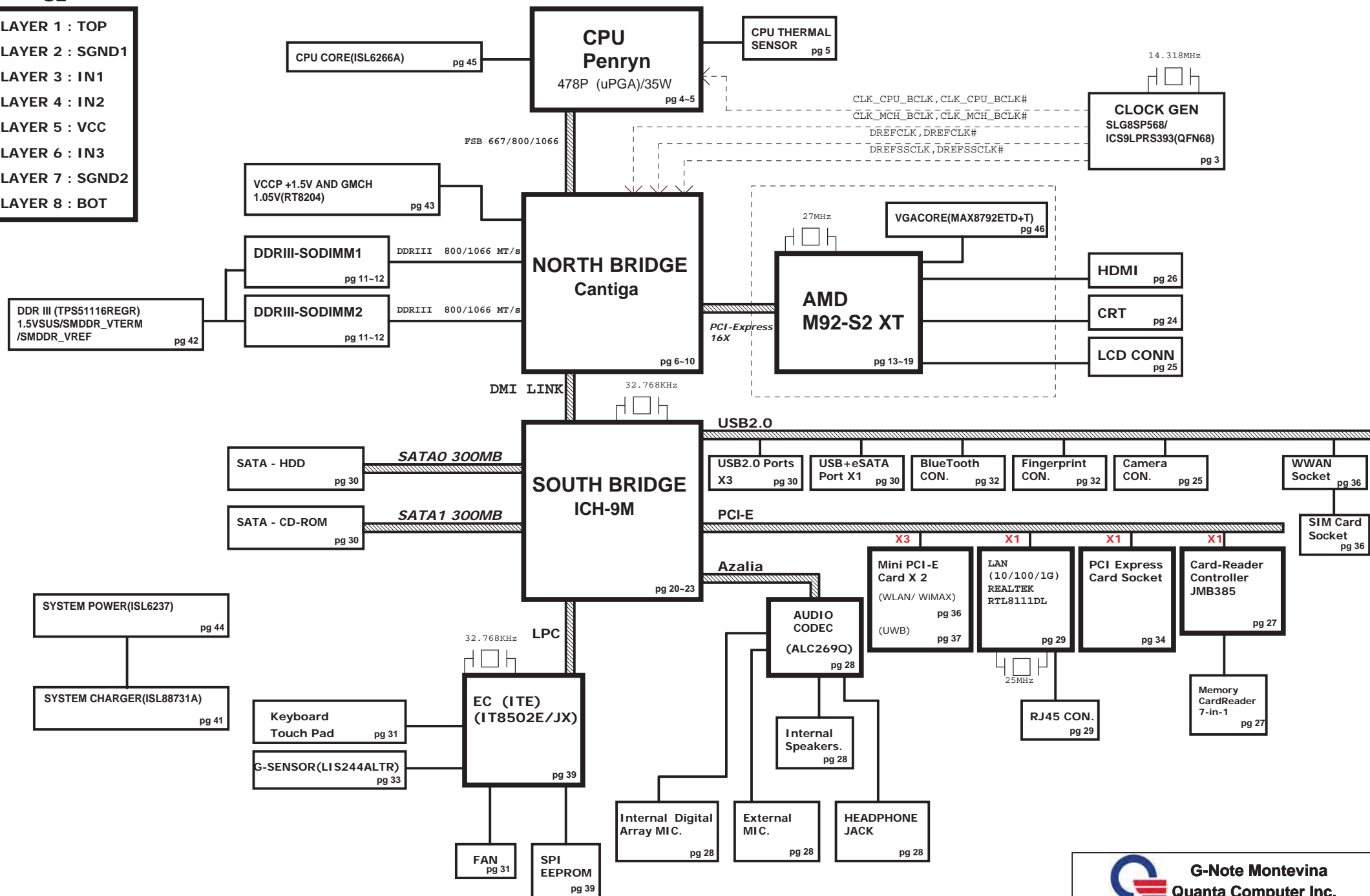


LAYER 1 : TOP
LAYER 2 : SGND1
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : IN3
LAYER 7 : SGND2
LAYER 8 : BOT




Will chang after circuit
finished

Power States

02

POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
VIN	10V~+19V	25,40,41,42,43,44,45,46,47	MAIN POWER		S0~S5
+3VRTC	+3.0V~+3.3V	20,23,39	RTC		S0~S5
3VPCU	+3.3V	20,25,29,31,37,39,40,41,44	8051 POWER		S0~S5
5VPCU	+5V	27,30,37,40,41,42,43,44,45,46,47	LCD/CHARGE POWER		S0~S5
+15V	+15V	25,40,44,47	LARGE POWER	5VPCU	S0~S5
LANVCC	+3.3V	29,40	LAN POWER	LAN_ON	
5VSUS	+5V	25,30,37,40,43,45,46	SLP_S5# CTRLD POWER	SUSON	
3VSUS	+3.3V	21,22,34,35,36,39,40,45	SLP_S5# CTRLD POWER	SUSON	
1.8VSUS	+1.8V	40,43,47	SODIMM POWER	SUSON	
+0.9V_DDR_VTT	+0.9V		SODIMM POWER	MAINON	
+5V	+5V	23,24,25,26,28,30,31,39,40,41	SLP_S3# CTRLD POWER	MAINON	
+3V	+3.3V	3,5,7,10,11,12,14,20,21,22,23,24,25,26,27,28,29,30,31,32,33,34,35,36,37,38,39,40,41,42,43,44,45,47	SLP_S3# CTRLD POWER	MAINON	
+1.8V	+1.8V	14,15,16,17,18,19,47	VGA POWER	MAINON	
+1.5V	+1.5V	5,10,20,21,22,23,34,35,36,40,43	CALISTOGA/ICH8 POWER	MAINON	
+1.05V	+1.05V	3,4,5,6,7,9,10,20,23,40,43,45	CPU/CALISTOGA/ICH8 POWER	MAINON	
VCC_CORE	+0.7V~+1.77V	4,5,40,45	CPU CORE POWER	VRON	
LCDVCC	+3.3V	25	LCD Power	INT_DISP_ON & EXT_LVDS_DIGON	
+5VHDD	+5V	30	HDD Power	MAINON	
MBATV	+10V~+17V	39,41	MAIN BATTERY	D/C#	



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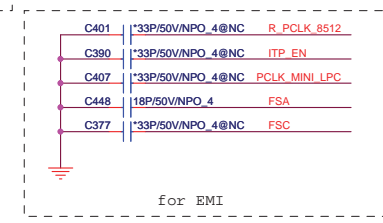
Size
B

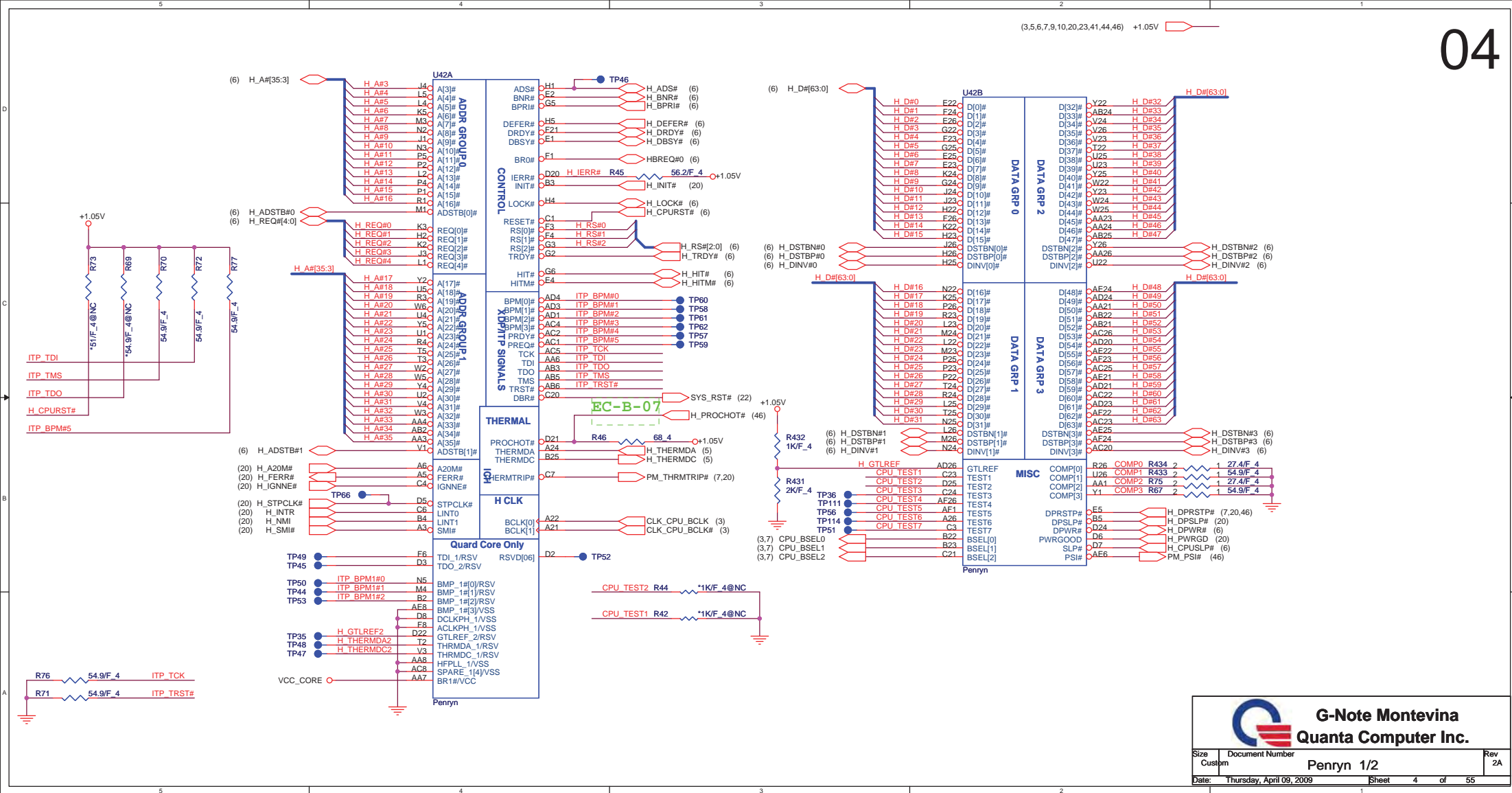
Document Number

Rev
2A

FRON TPAGE

Date: Tuesday, March 03, 2009Sheet 2 of 55



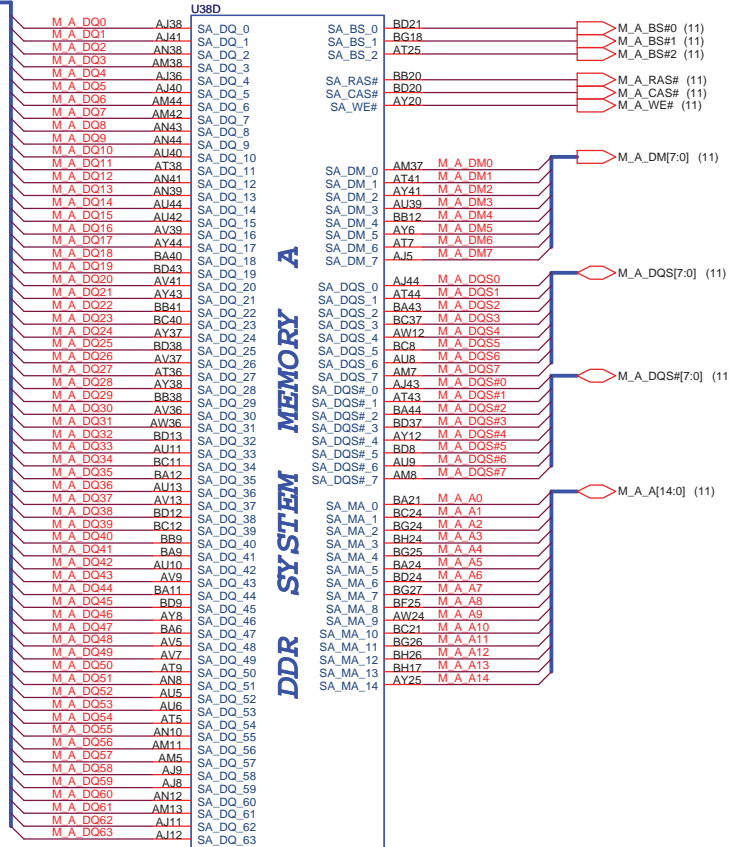






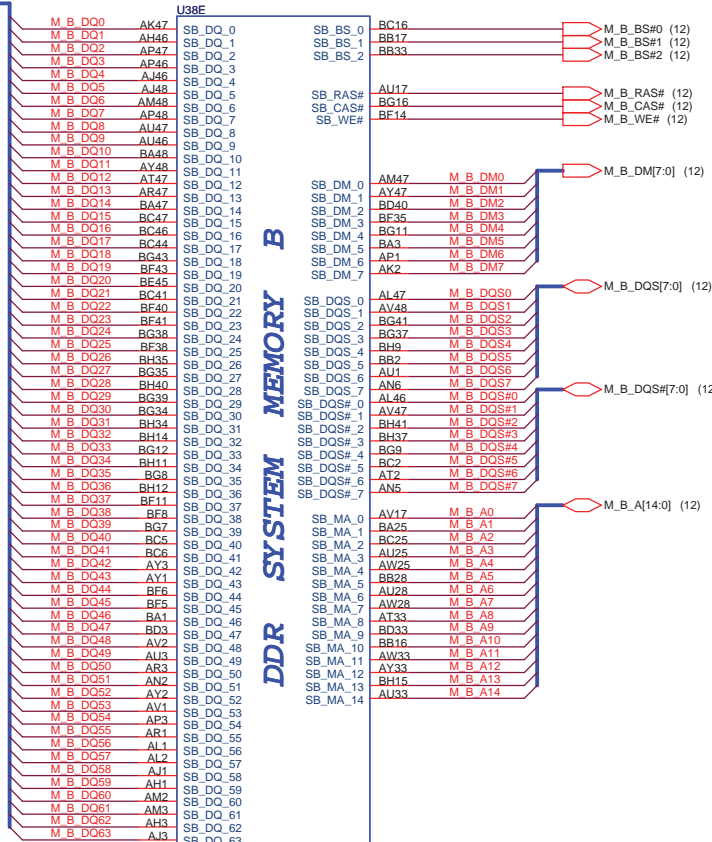


(11) M_A_DQ[63:0]



CANTIGA_PM

(12) M_B_DQ[63:0]

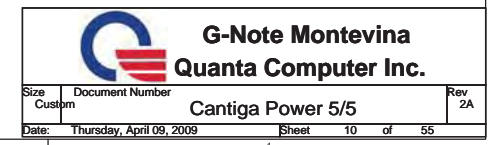


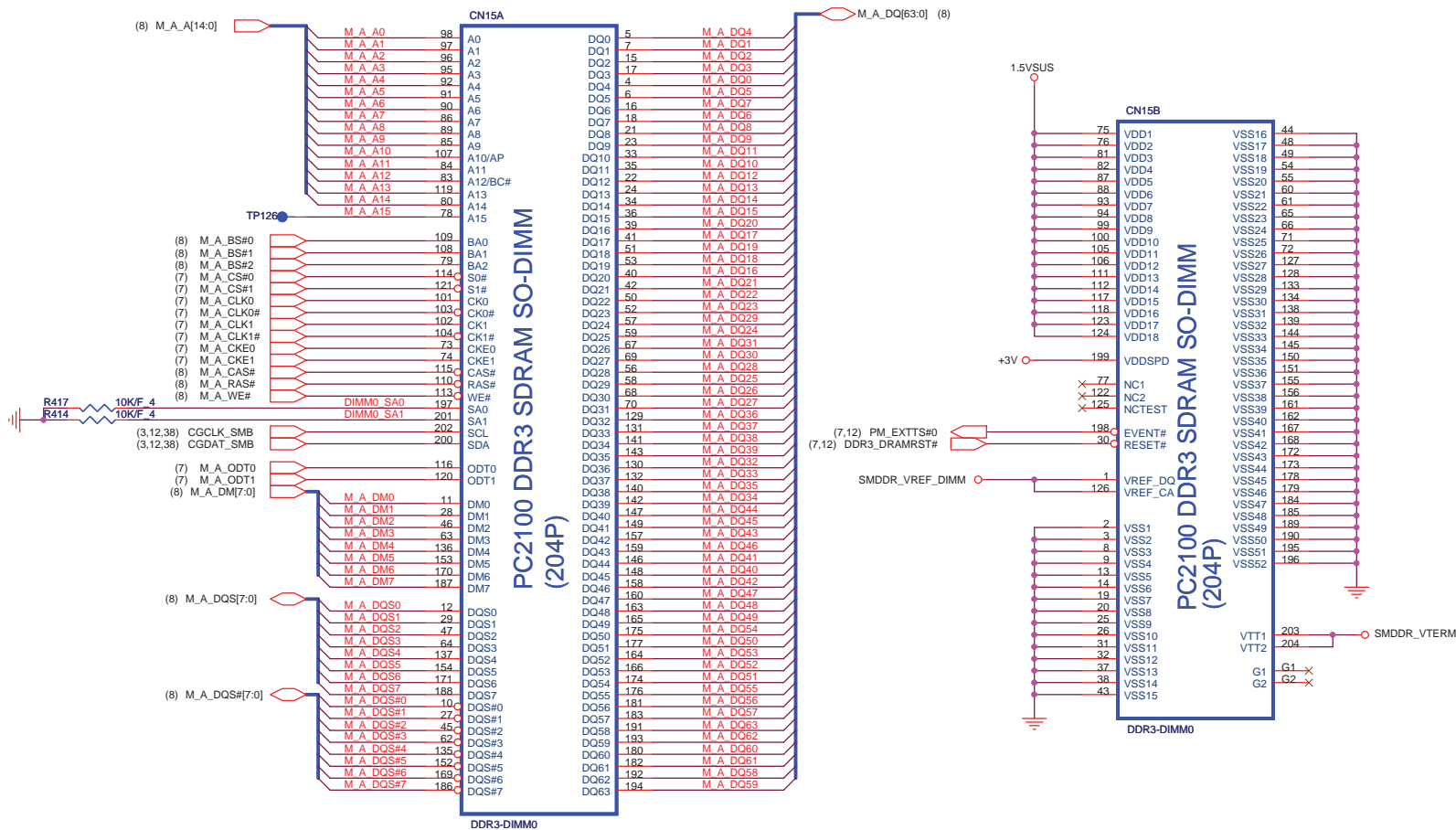
CANTIGA_PM

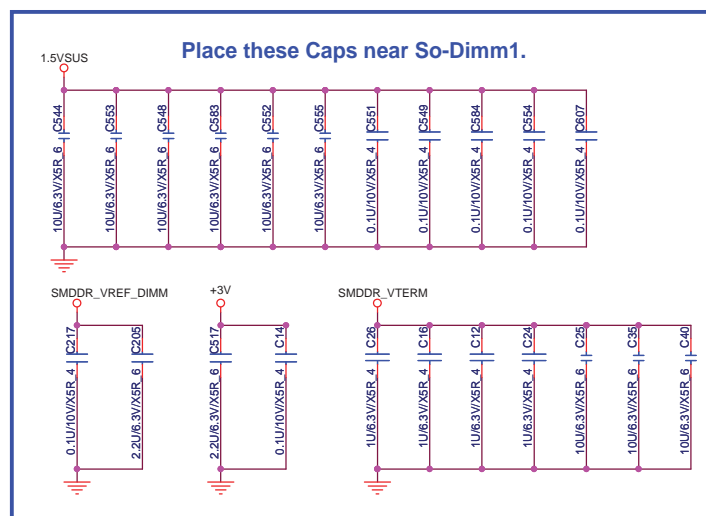
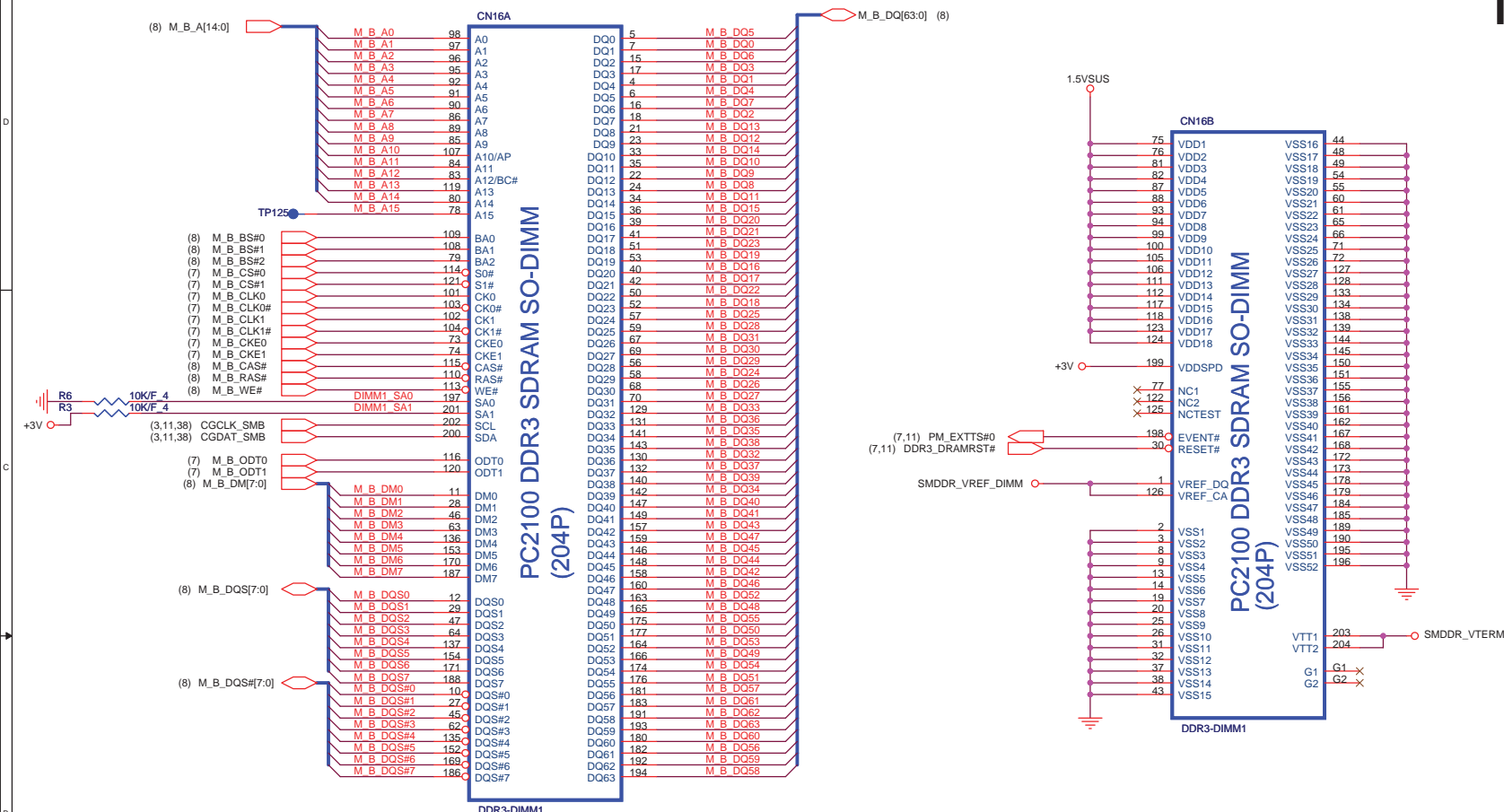


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(7) PEG_TX[0..15]
(7) PEG_TX#0..15



PEG_TX0	AF30	PCIE_RX0P
PEG_TX#0	AE31	PCIE_RX0N
PEG_TX1	AE29	PCIE_RX1P
PEG_TX#1	AD28	PCIE_RX1N
PEG_TX2	AD30	PCIE_RX2P
PEG_TX#2	AC31	PCIE_RX2N
PEG_TX3	AC29	PCIE_RX3P
PEG_TX#3	AB28	PCIE_RX3N
PEG_TX4	AB30	PCIE_RX4P
PEG_TX#4	AA31	PCIE_RX4N
PEG_TX5	AA29	PCIE_RX5P
PEG_TX#5	Y28	PCIE_RX5N
PEG_TX6	Y30	PCIE_RX6P
PEG_TX#6	W31	PCIE_RX6N
PEG_TX7	W29	PCIE_RX7P
PEG_TX#7	V28	PCIE_RX7N
PEG_TX8	V30	PCIE_RX8P
PEG_TX#8	U31	PCIE_RX8N
PEG_TX9	U29	PCIE_RX9P
PEG_TX#9	T28	PCIE_RX9N
PEG_TX10	T30	PCIE_RX10P
PEG_TX#10	R31	PCIE_RX10N
PEG_TX11	R29	PCIE_RX11P
PEG_TX#11	P28	PCIE_RX11N
PEG_TX12	P30	PCIE_RX12P
PEG_TX#12	N31	PCIE_RX12N
PEG_TX13	N29	PCIE_RX13P
PEG_TX#13	M28	PCIE_RX13N
PEG_TX14	M30	PCIE_RX14P
PEG_TX#14	L31	PCIE_RX14N
PEG_TX15	L29	PCIE_RX15P
PEG_TX#15	K30	PCIE_RX15N

(3) CLK_PCIE_VGA
(3) CLK_PCIE_VGA#



(21) PLTRST_DELAY# **EC-B-07**

AL27

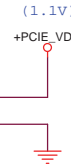
U44A

PART 1 OF 10

PCI-EXPRESS INTERFACE

PCIE_TX0P	AH30	PEG_C_RXP0
PCIE_TX0N	AG31	PEG_C_RXN0
PCIE_TX1P	AG29	PEG_C_RXP1
PCIE_TX1N	AF28	PEG_C_RXN1
PCIE_TX2P	AF27	PEG_C_RXP2
PCIE_TX2N	AE26	PEG_C_RXN2
PCIE_TX3P	AD27	PEG_C_RXP3
PCIE_TX3N	AD26	PEG_C_RXN3
PCIE_TX4P	AC25	PEG_C_RXP4
PCIE_TX4N	AB25	PEG_C_RXN4
PCIE_TX5P	Y23	PEG_C_RXP5
PCIE_TX5N	Y24	PEG_C_RXN5
PCIE_TX6P	AB27	PEG_C_RXP6
PCIE_TX6N	AB26	PEG_C_RXN6
PCIE_TX7P	Y27	PEG_C_RXP7
PCIE_TX7N	Y26	PEG_C_RXN7
PCIE_TX8P	W24	PEG_C_RXP8
PCIE_TX8N	W23	PEG_C_RXN8
PCIE_TX9P	V27	PEG_C_RXP9
PCIE_TX9N	U26	PEG_C_RXN9
PCIE_TX10P	U24	PEG_C_RXP10
PCIE_TX10N	U23	PEG_C_RXN10
PCIE_TX11P	T26	PEG_C_RXP11
PCIE_TX11N	T27	PEG_C_RXN11
PCIE_TX12P	T24	PEG_C_RXP12
PCIE_TX12N	T23	PEG_C_RXN12
PCIE_TX13P	P27	PEG_C_RXP13
PCIE_TX13N	P26	PEG_C_RXN13
PCIE_TX14P	P24	PEG_C_RXP14
PCIE_TX14N	P23	PEG_C_RXN14
PCIE_TX15P	M27	PEG_C_RXP15
PCIE_TX15N	M26	PEG_C_RXN15

PCIE_CALRN	AA22	PCIE_CALRN	2K/F	R441
PCIE_CALRP	Y22	PCIE_CALRP	1.27K/F	R56



(7) PEG_RX[0..15]
(7) PEG_RX#0..15



PEG_RX0	0.1U/10V/X7R	4	2	1	C115	10	PEG_C_RXP0
PEG_RX1	0.1U/10V/X7R	4	2	1	C96	10	PEG_C_RXP1
PEG_RX2	0.1U/10V/X7R	4	2	1	C117	10	PEG_C_RXP2
PEG_RX3	0.1U/10V/X7R	4	2	1	C86	10	PEG_C_RXP3
PEG_RX4	0.1U/10V/X7R	4	2	1	C108	10	PEG_C_RXP4
PEG_RX5	0.1U/10V/X7R	4	2	1	C90	10	PEG_C_RXP5
PEG_RX6	0.1U/10V/X7R	4	2	1	C87	10	PEG_C_RXP6
PEG_RX7	0.1U/10V/X7R	4	2	1	C111	10	PEG_C_RXP7
PEG_RX8	0.1U/10V/X7R	4	2	1	C119	10	PEG_C_RXP8
PEG_RX9	0.1U/10V/X7R	4	2	1	C121	10	PEG_C_RXP9
PEG_RX10	0.1U/10V/X7R	4	2	1	C99	10	PEG_C_RXP10
PEG_RX11	0.1U/10V/X7R	4	2	1	C100	10	PEG_C_RXP11
PEG_RX12	0.1U/10V/X7R	4	2	1	C123	10	PEG_C_RXP12
PEG_RX13	0.1U/10V/X7R	4	2	1	C92	10	PEG_C_RXP13
PEG_RX14	0.1U/10V/X7R	4	2	1	C94	10	PEG_C_RXP14
PEG_RX15	0.1U/10V/X7R	4	2	1	C113	10	PEG_C_RXP15
PEG_RX#0	0.1U/10V/X7R	4	2	1	C116	10	PEG_C_RXN0
PEG_RX#1	0.1U/10V/X7R	4	2	1	C97	10	PEG_C_RXN1
PEG_RX#2	0.1U/10V/X7R	4	2	1	C118	10	PEG_C_RXN2
PEG_RX#3	0.1U/10V/X7R	4	2	1	C85	10	PEG_C_RXN3
PEG_RX#4	0.1U/10V/X7R	4	2	1	C109	10	PEG_C_RXN4
PEG_RX#5	0.1U/10V/X7R	4	2	1	C89	10	PEG_C_RXN5
PEG_RX#6	0.1U/10V/X7R	4	2	1	C88	10	PEG_C_RXN6
PEG_RX#7	0.1U/10V/X7R	4	2	1	C110	10	PEG_C_RXN7
PEG_RX#8	0.1U/10V/X7R	4	2	1	C120	10	PEG_C_RXN8
PEG_RX#9	0.1U/10V/X7R	4	2	1	C122	10	PEG_C_RXN9
PEG_RX#10	0.1U/10V/X7R	4	2	1	C98	10	PEG_C_RXN10
PEG_RX#11	0.1U/10V/X7R	4	2	1	C101	10	PEG_C_RXN11
PEG_RX#12	0.1U/10V/X7R	4	2	1	C124	10	PEG_C_RXN12
PEG_RX#13	0.1U/10V/X7R	4	2	1	C93	10	PEG_C_RXN13
PEG_RX#14	0.1U/10V/X7R	4	2	1	C95	10	PEG_C_RXN14
PEG_RX#15	0.1U/10V/X7R	4	2	1	C114	10	PEG_C_RXN15

100 MHz (+/-300 ppm) input frequency,
0-0.7 V single-ended swing.
clock must be provided less than 400ns
after CLKREQ# is asserted

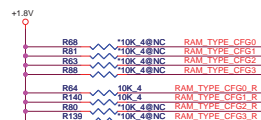
M92-S2/M92-XT

MEMORY APERTURE SIZE SELECT			
MEMORY SIZE	CFG2 GPIO13	CFG1 GPIO12	CFG0 GPIO11
128MB	0	0	0
256MB	0	0	1
64MB	0	1	0

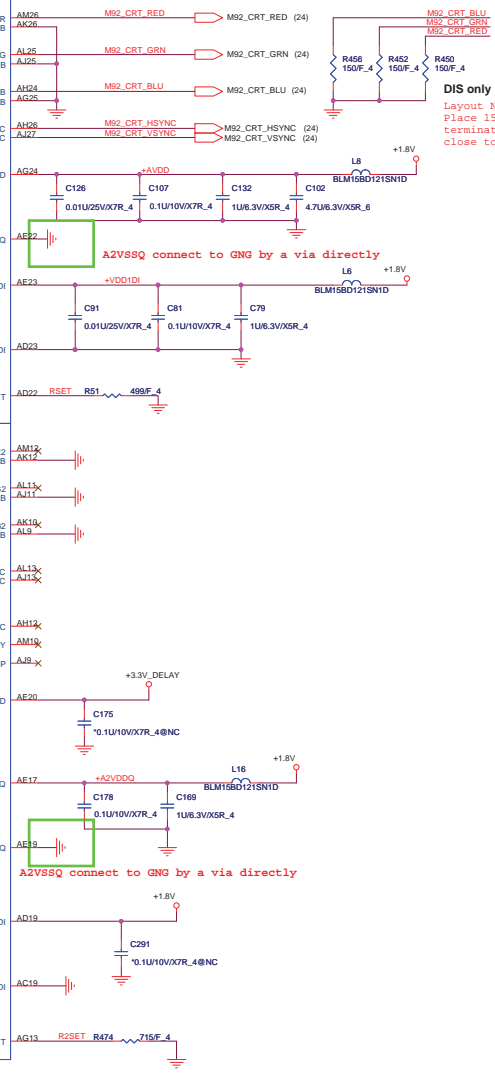
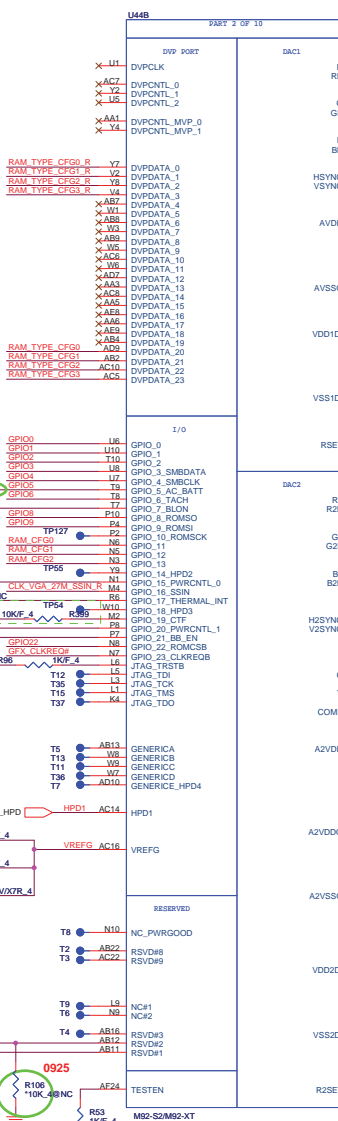
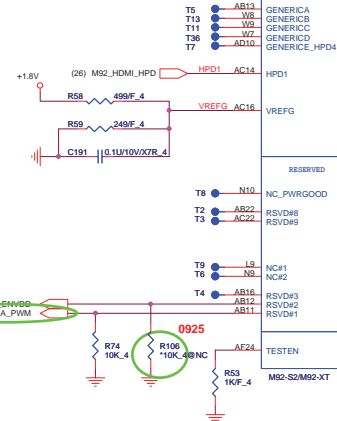
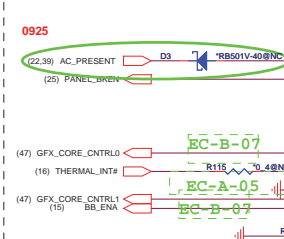
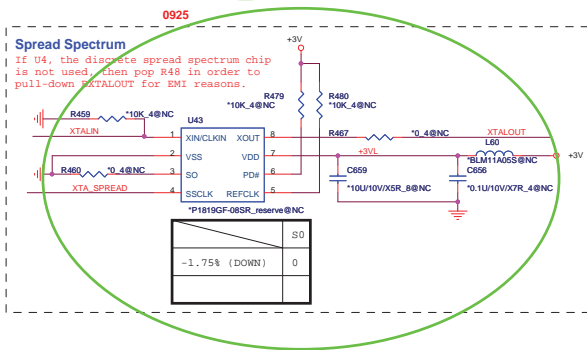
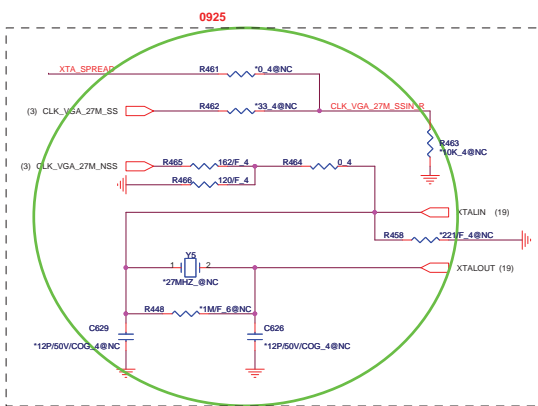
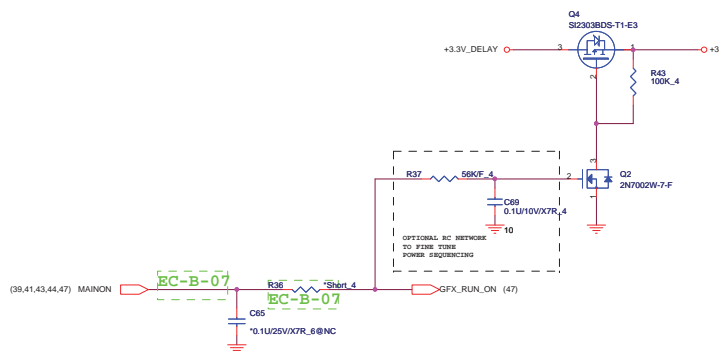
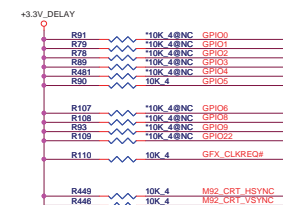
Remove 512MB



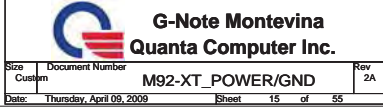
Memory Straps	RAM_TYPE_CFG3_R	RAM_TYPE_CFG2_R	RAM_TYPE_CFG1_R	RAM_TYPE_CFG0_R
900MHz 128MB(32M*32)				
Hyinix H5RS1H123MFR-11C	0	0	0	0
900MHz 128MB(32M*32)				
Qimonda HYB18H1G321AF-11	0	0	0	1
900MHz 128MB(32M*32)				
Qimonda HYB18H1G321A2F-11	0	0	1	1
900MHz 128MB(32M*32)				
Samsung K4H103240D-HC12	0	1	0	0



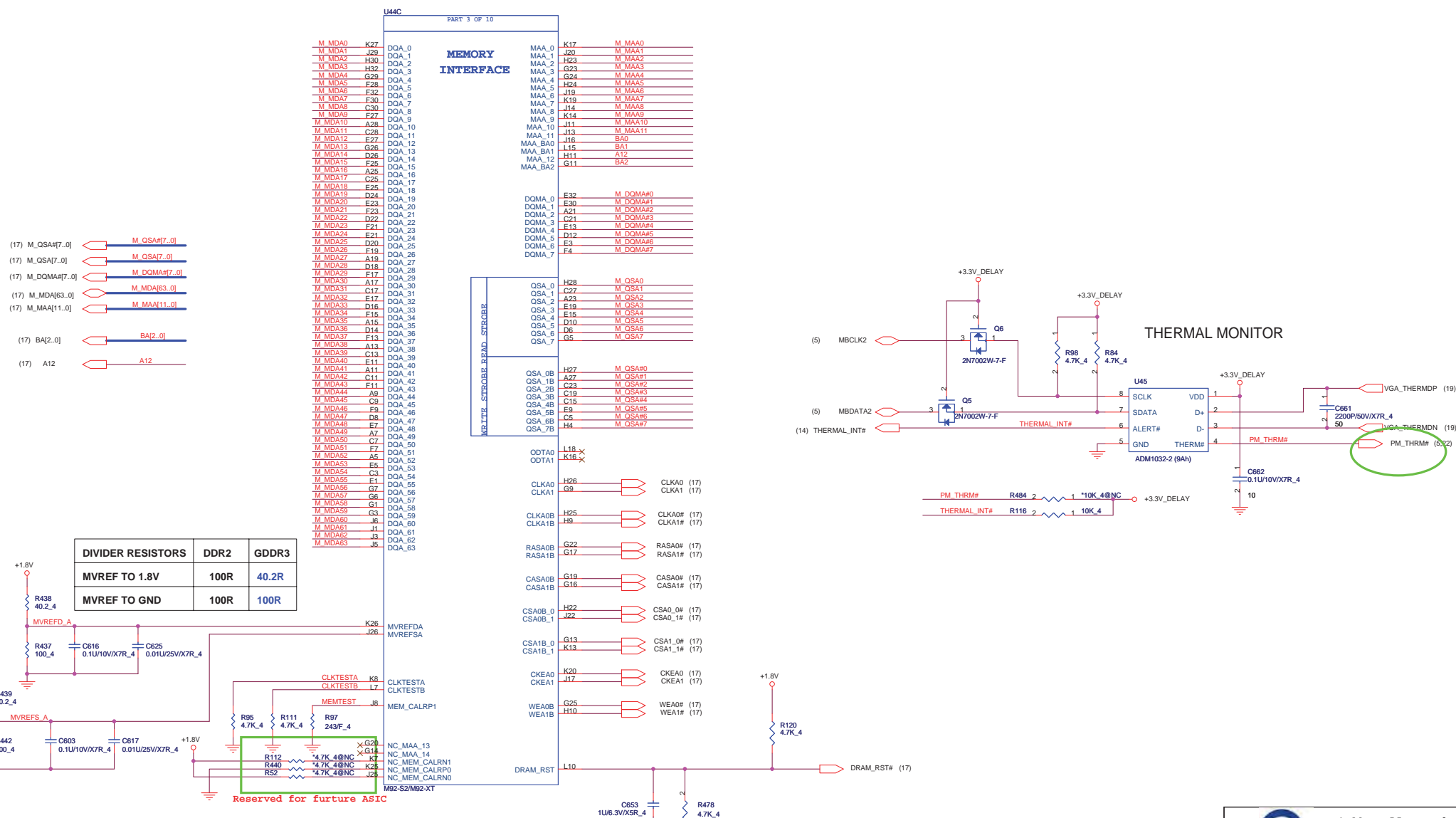
GPIO Straps table	DESCRIPTION OF DEFAULT SETTINGS	G Note Setting
GPIO0	GPIO[0] - TX_PWRS_ENB (Transmitter Power Savings Enable) 0: SW TX output saving for mobile mode 1: SW TX output saving (Default setting for Desktop)	0
GPIO1	GPIO[1] - TX_DEEMPH_EN (Transmitter De-emphasis Enable) 0: TX de-emphasis disabled for mobile mode 1: TX de-emphasis enabled (Default setting for Desktop)	0
GPIO2	GPIO2 - BP_GEN2_EN (BP Gen2 Enable) 0: Default (Driver Controlled Gen2) 1: Strap Controlled Gen2	0
GPIO3	ATI reserved configuration straps.	0
GPIO4	ATI reserved configuration straps.	0
GPIO5	GPIO_5_AC_BATT 0: Battery saving mode = 0.0 V 1: AC (Performance mode) = 3.3 V	1
GPIO6	ATI Internal use only	0
GPIO8	ATI reserved configuration straps.	0
GPIO9	VGA Disable 0 - VGA Controller capacity enabled 1 - The device will not recognized as the system's VGA controller	0
GPIO22	Enable external BIOS ROM device 0 - Disable external BIOS ROM device 1 - Enable external BIOS ROM device	0
HSYNC	AUD[3] 03 03 - No audio function	1
VSNC	11 - Audio for DisplayPort and HDMI if detector is detected 10 - Audio for DisplayPort only 11 - Audio for both DisplayPort and HDMI	1



DIS only
Layout Note:
Place 150 ohm
termination resistors
close to ATI CHIP.

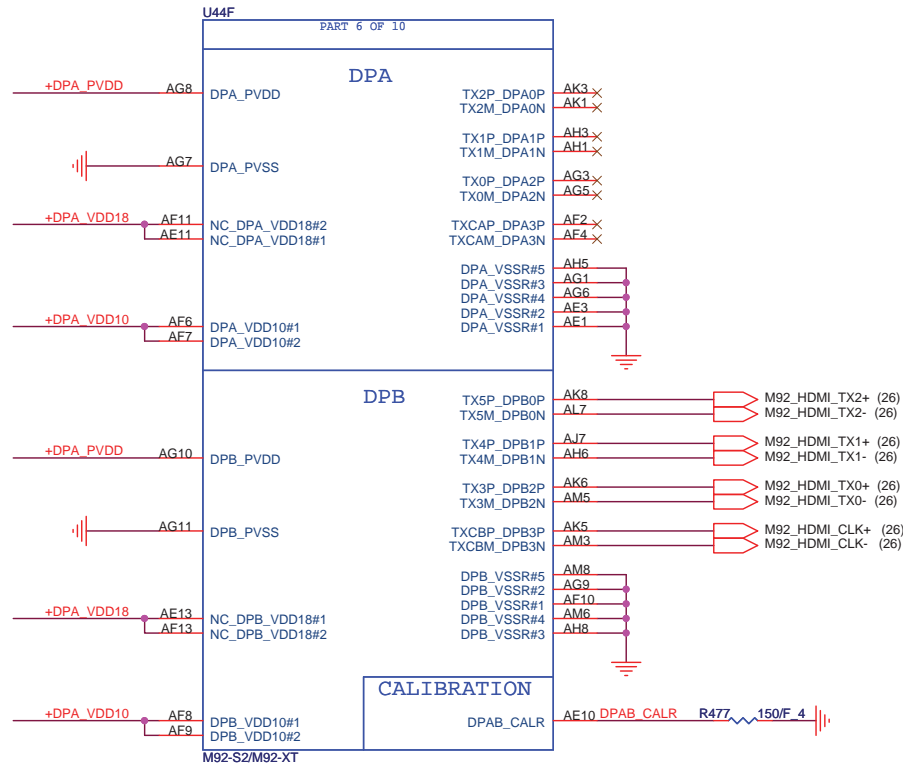


MEMORY INTERFACE

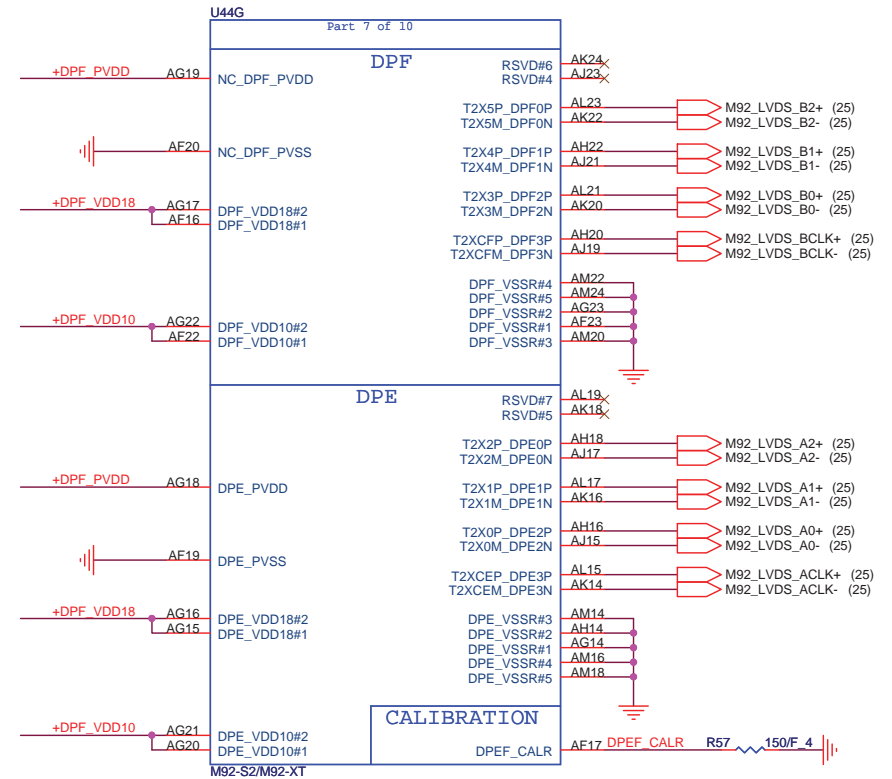




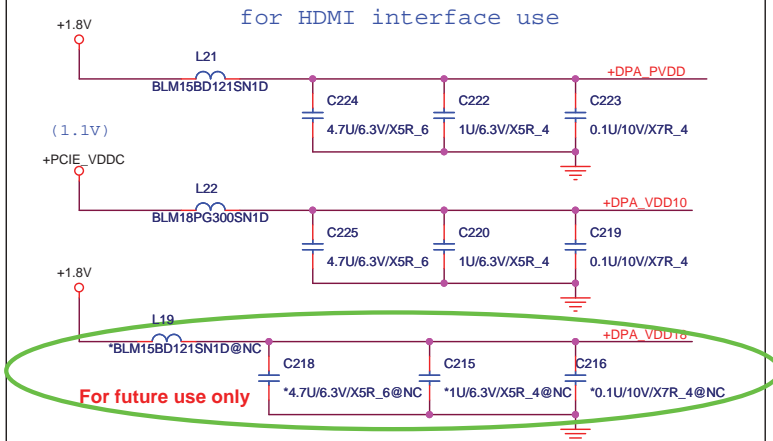
TMDP(HDMI) INTERFACE



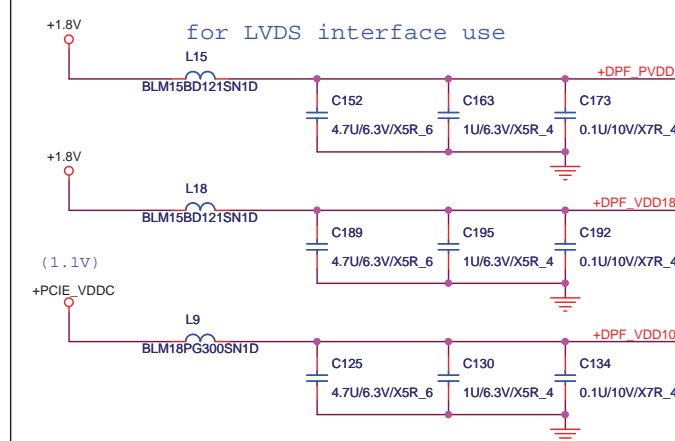
LVDS INTERFACE



for HDMI interface use

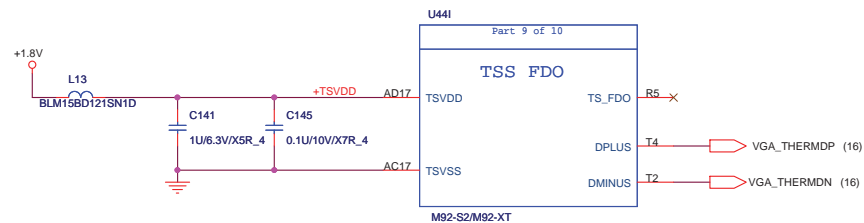
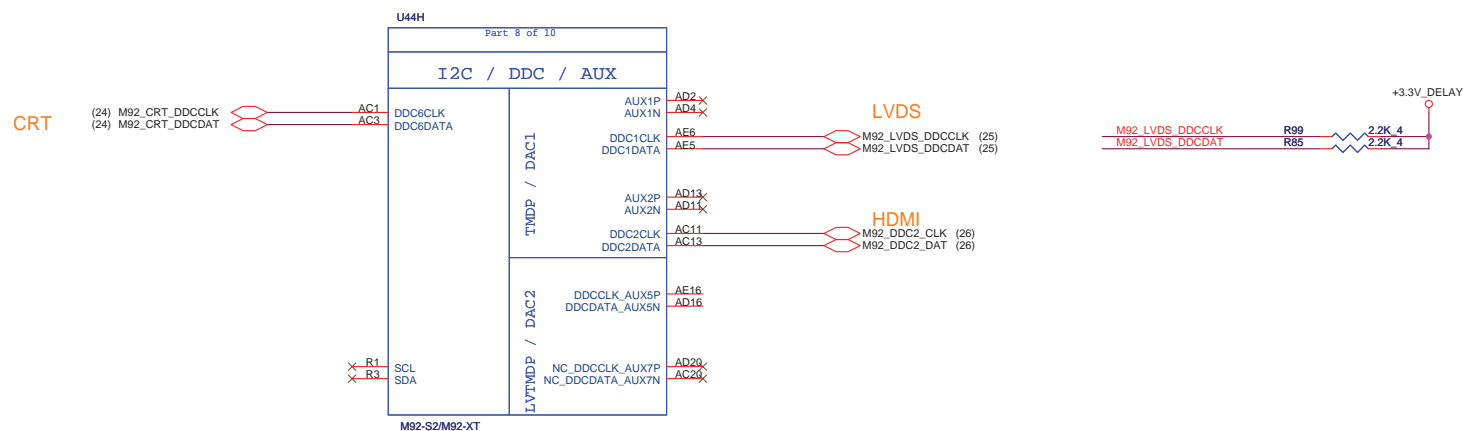
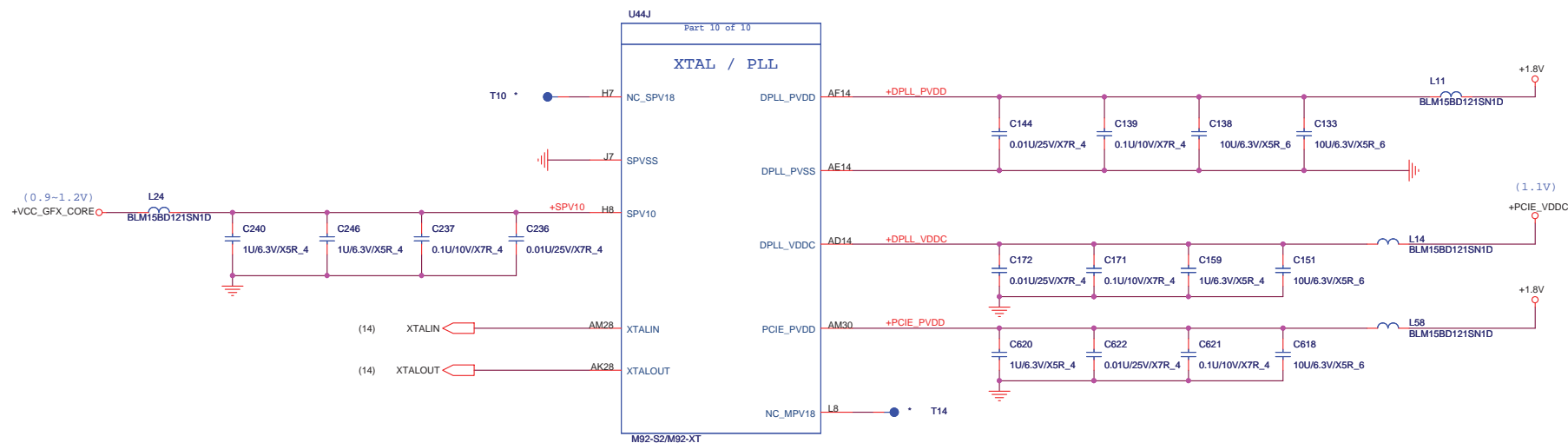


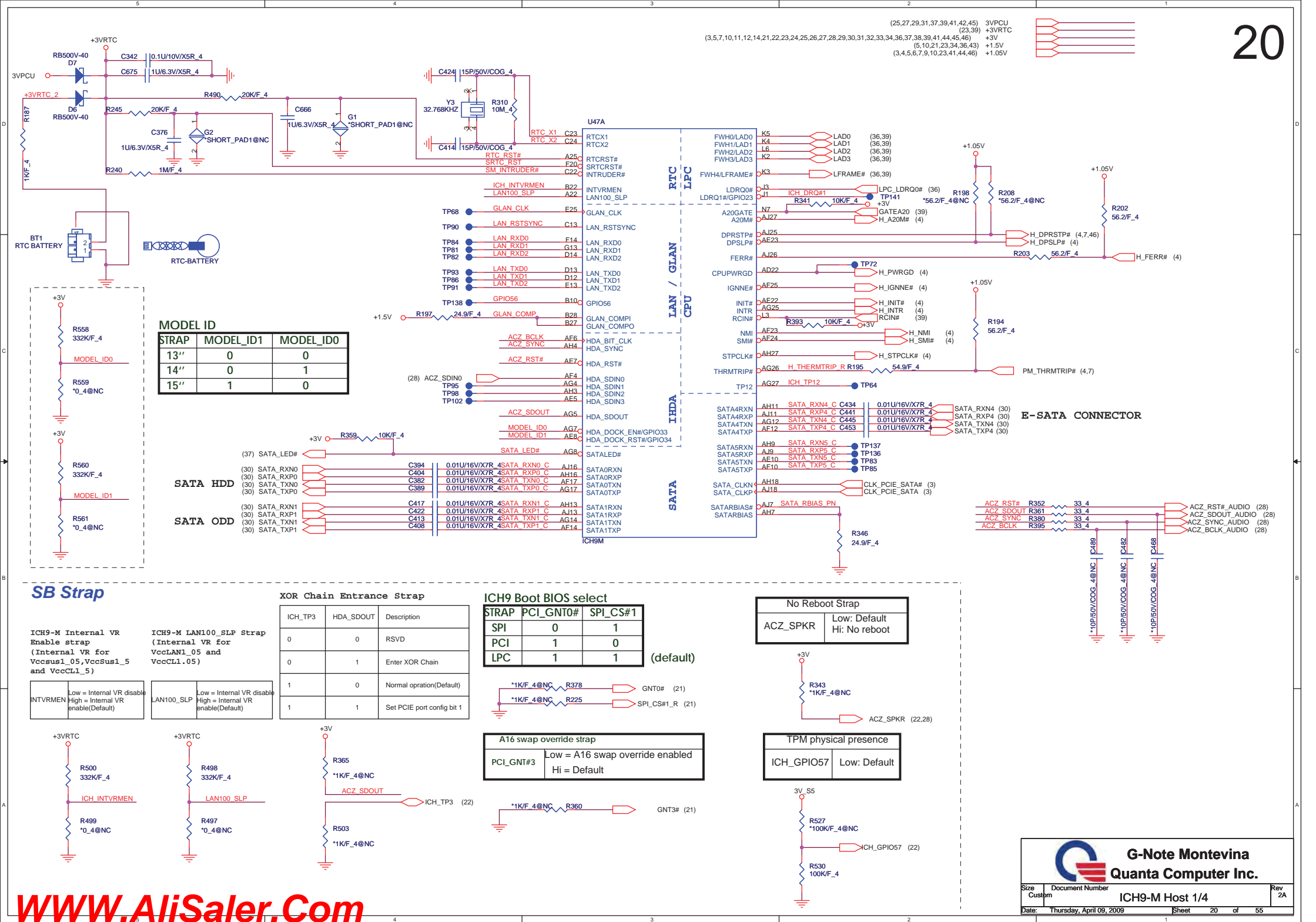
for LVDS interface use



G-Note Montevina
Quanta Computer Inc.

Size B	Document Number	M92-XT_TMDP I/F	Rev 2A
Date: Thursday, April 09, 2009	Sheet 18	of 55	





CARD-READER

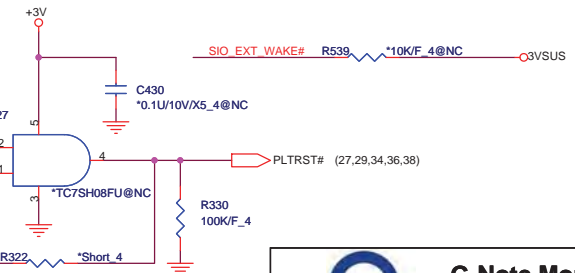
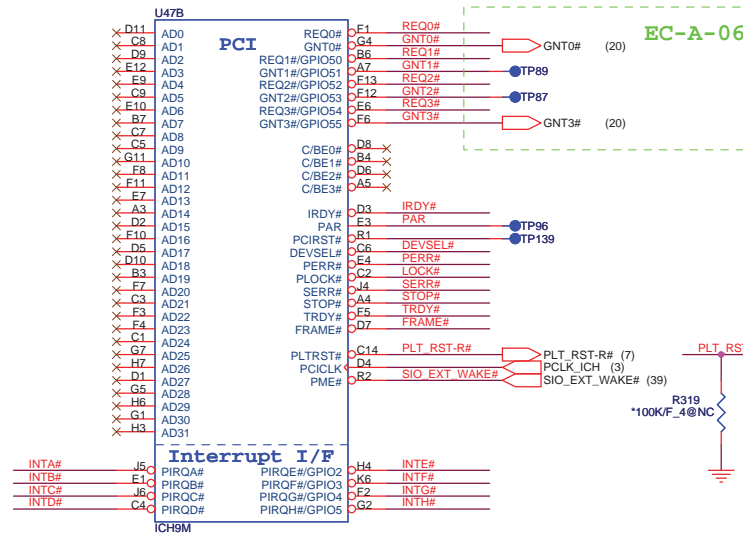
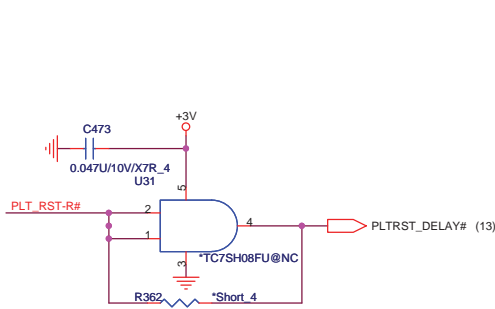
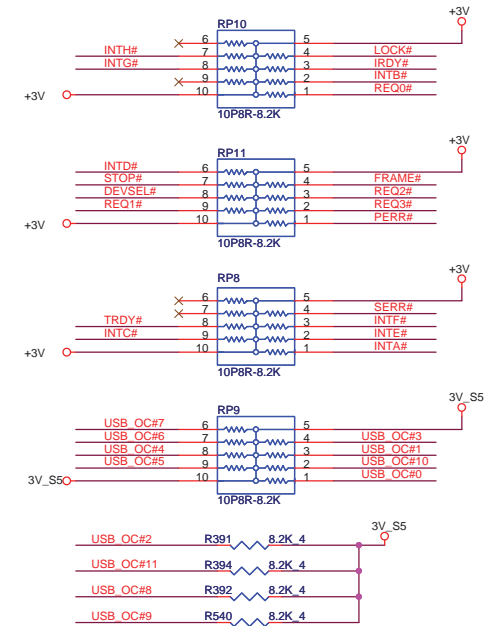
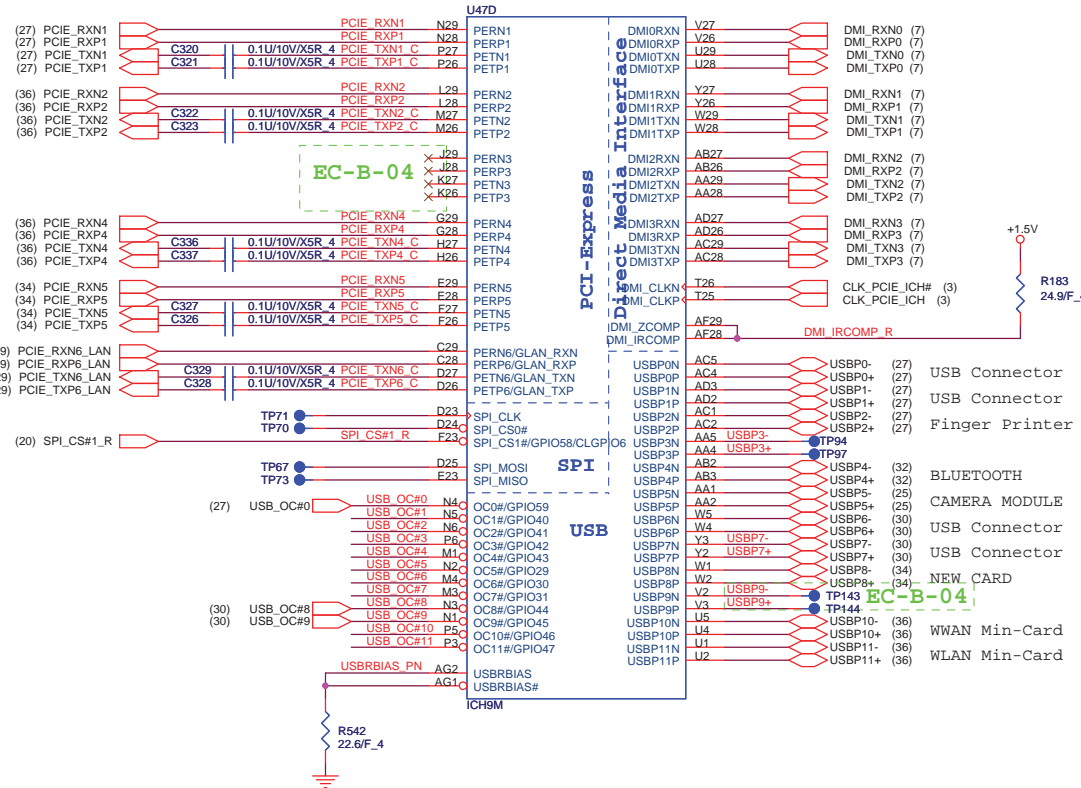
MINI CARD PCI-E(WWAN)

MINI CARD PCI-E(WLAN)

EXPRESS CARD (NEW CARD)

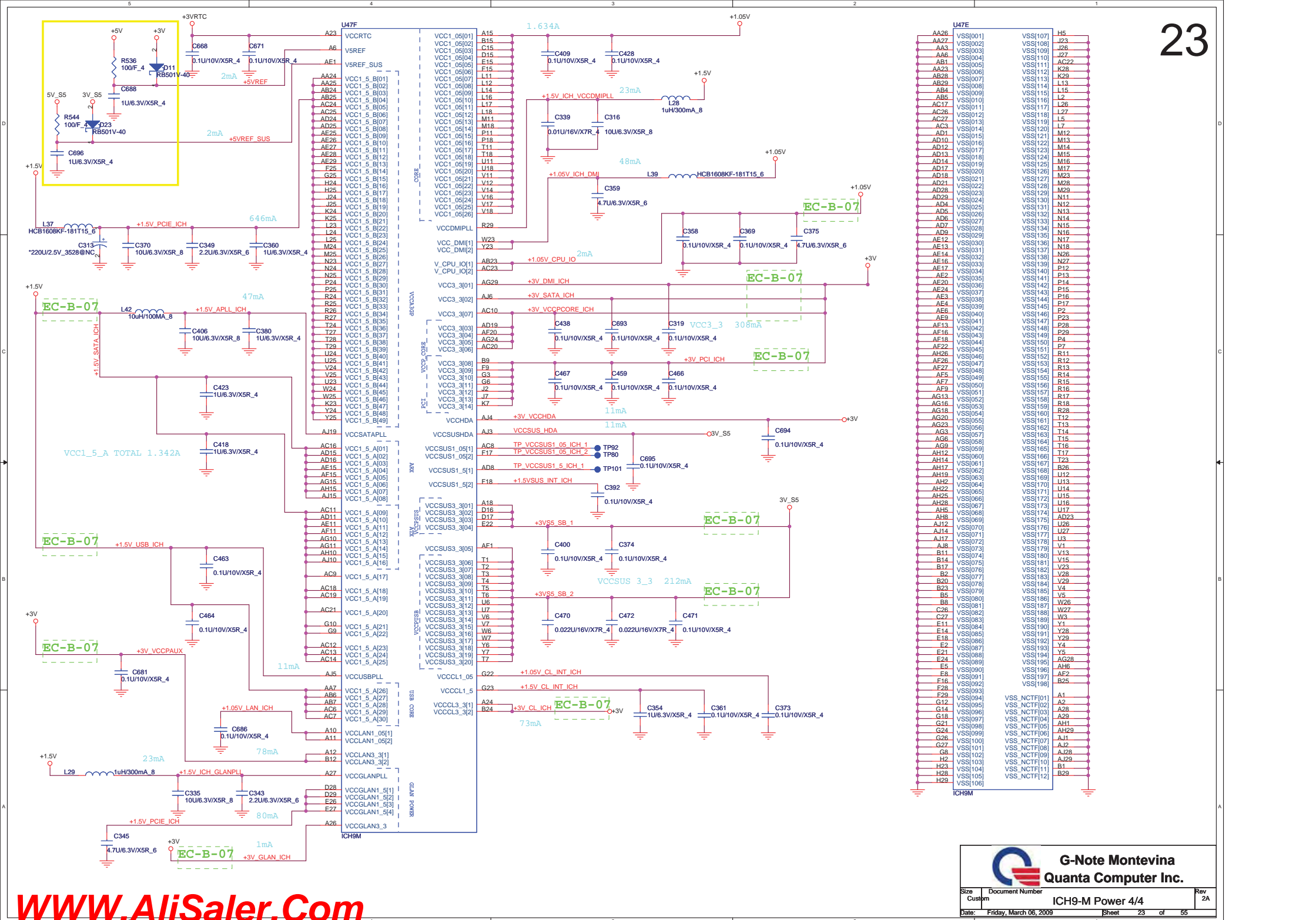
PCIE-LAN

(5,10,20,23,34,36,43) +1.5V
 (3,5,7,10,11,12,14,20,22,23,24,25,26,27,28,29,30,31,32,33,34,36,37,38,39,41,44,45,46) +3V
 (22,27,34,39,41,43,46,47) 3VSUS



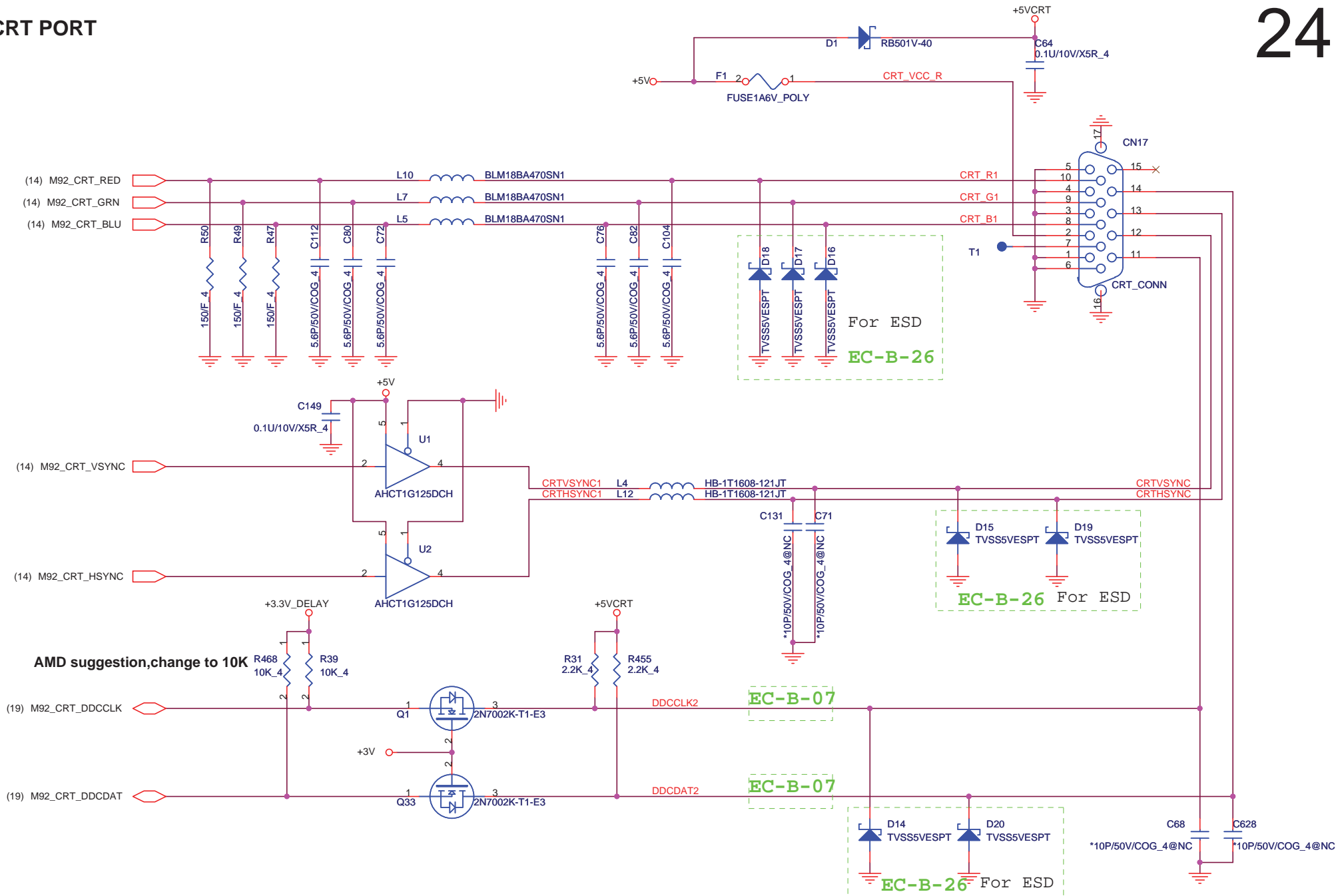
PCI ROUTING TABLE	IDSEL	INTERRUPT	DEVICE
REQ0#	AD2	INT#	R5C847
REQ1#	AD3	INT#	R5C847
REQ2#	AD4	INT#	R5C847
REQ3#	AD5	INT#	R5C847
REQ4#	AD6	INT#	R5C847
REQ5#	AD7	INT#	R5C847
REQ6#	AD8	INT#	R5C847
REQ7#	AD9	INT#	R5C847
REQ8#	AD10	INT#	R5C847
REQ9#	AD11	INT#	R5C847
REQ10#	AD12	INT#	R5C847
REQ11#	AD13	INT#	R5C847
REQ12#	AD14	INT#	R5C847
REQ13#	AD15	INT#	R5C847
REQ14#	AD16	INT#	R5C847
REQ15#	AD17	INT#	R5C847
REQ16#	AD18	INT#	R5C847
REQ17#	AD19	INT#	R5C847
REQ18#	AD20	INT#	R5C847
REQ19#	AD21	INT#	R5C847
REQ20#	AD22	INT#	R5C847
REQ21#	AD23	INT#	R5C847
REQ22#	AD24	INT#	R5C847
REQ23#	AD25	INT#	R5C847
REQ24#	AD26	INT#	R5C847
REQ25#	AD27	INT#	R5C847
REQ26#	AD28	INT#	R5C847
REQ27#	AD29	INT#	R5C847
REQ28#	AD30	INT#	R5C847
REQ29#	AD31	INT#	R5C847



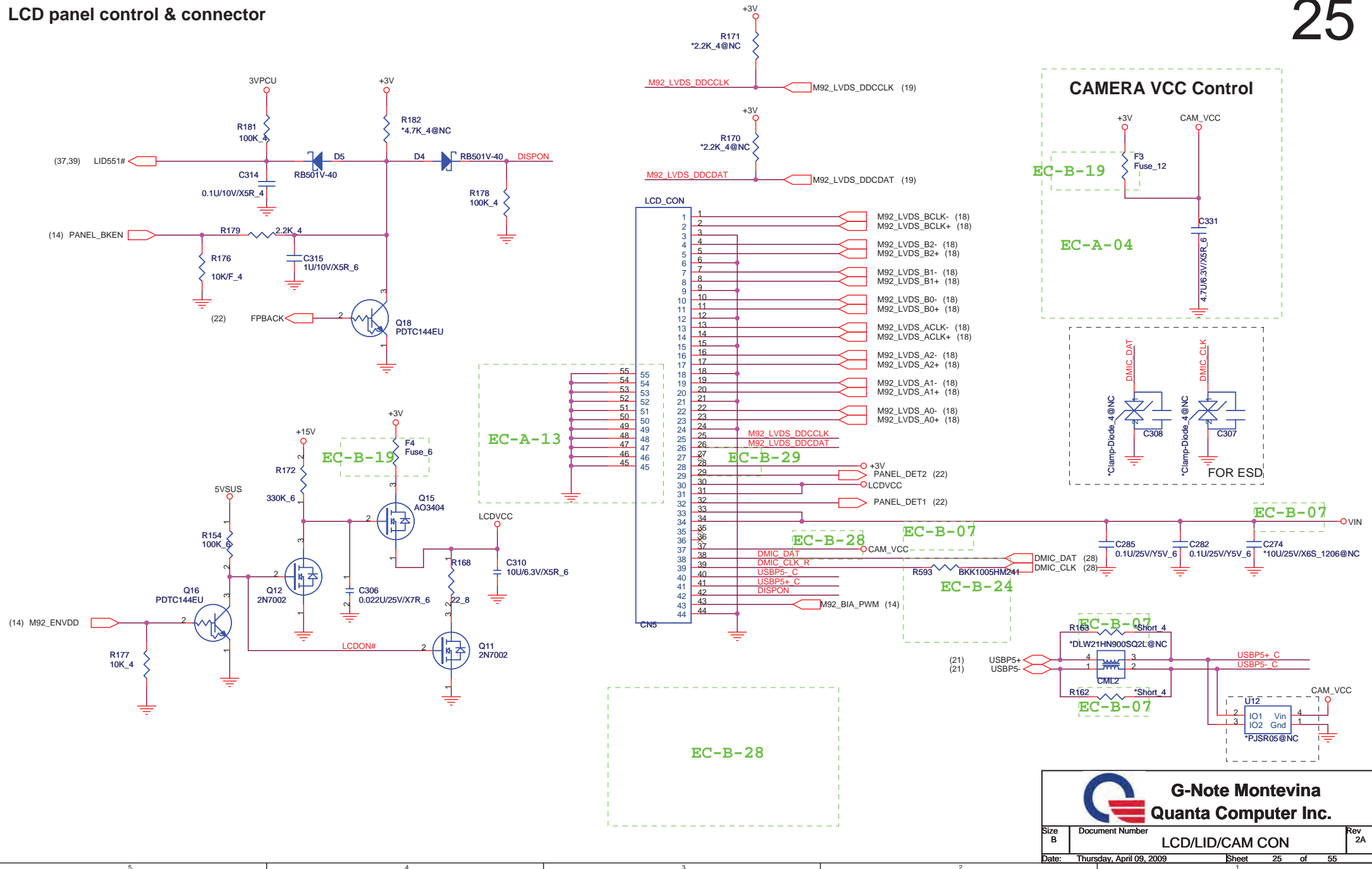


CRT PORT

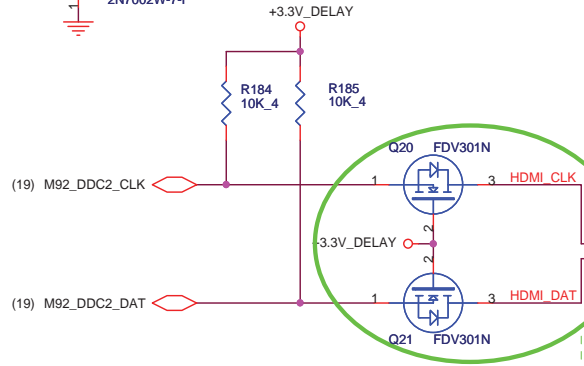
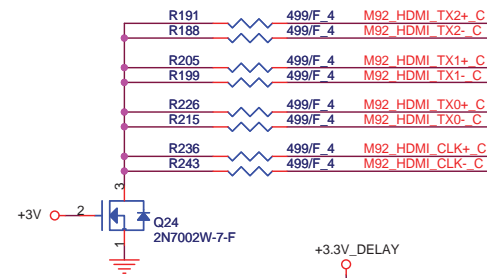
24



LCD panel control & connector

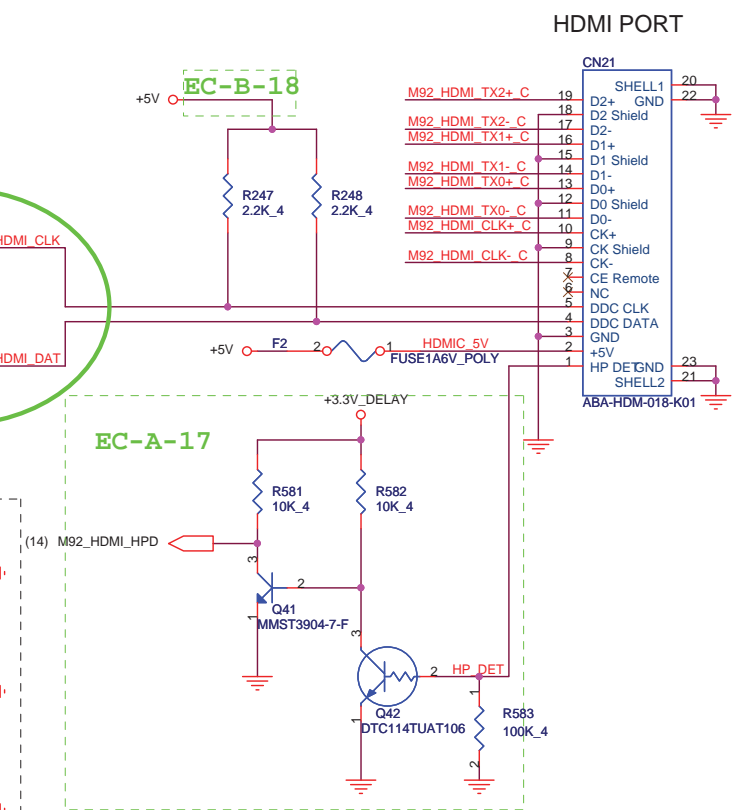


(18) M92_HDMI_TX2+	C332	0.1U/10V/X7R 4	M92_HDMI_TX2+ C
(18) M92_HDMI_TX2-	C330	0.1U/10V/X7R 4	M92_HDMI_TX2- C
(18) M92_HDMI_TX1+	C344	0.1U/10V/X7R 4	M92_HDMI_TX1+ C
(18) M92_HDMI_TX1-	C340	0.1U/10V/X7R 4	M92_HDMI_TX1- C
(18) M92_HDMI_TX0+	C355	0.1U/10V/X7R 4	M92_HDMI_TX0+ C
(18) M92_HDMI_TX0-	C352	0.1U/10V/X7R 4	M92_HDMI_TX0- C
(18) M92_HDMI_CLK+	C364	0.1U/10V/X7R 4	M92_HDMI_CLK+ C
(18) M92_HDMI_CLK-	C372	0.1U/10V/X7R 4	M92_HDMI_CLK- C



U18 *RClamp0514M_AG@NC		U21 *RClamp0514M_AG@NC		U22 *RClamp0514M_AG@NC	
M92_HDMI_TX2+ C	1	10	M92_HDMI_TX2+ C	1	10
M92_HDMI_TX2- C	2	9	M92_HDMI_TX2- C	2	9
M92_HDMI_TX1+ C	3	8	M92_HDMI_TX1+ C	3	8
M92_HDMI_TX1- C	4	7	M92_HDMI_TX1- C	4	7
M92_HDMI_TX0+ C	5	6	M92_HDMI_TX0+ C	5	6
M92_HDMI_TX0- C	1	10	M92_HDMI_TX0- C	1	10
M92_HDMI_CLK+ C	2	9	M92_HDMI_CLK+ C	2	9
M92_HDMI_CLK- C	3	8	M92_HDMI_CLK- C	3	8
HDMI_DAT	4	7	HDMI_DAT	4	7
HDMI_CLK	5	6	HDMI_CLK	5	6
HDMIC_5V	1	10	HDMIC_5V	1	10
HP_DET	2	9	HP_DET	2	9

For ESD Layout note: Place close to HDMI Conn



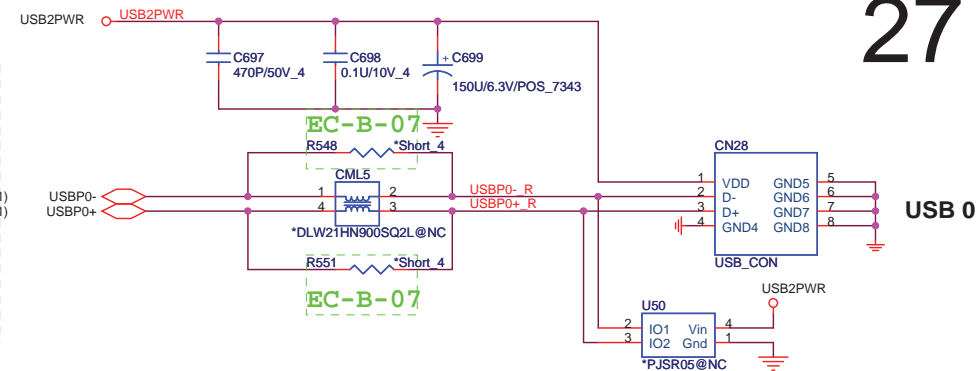
HDMI PORT

CN21	SHELL1	20	GND
D2+	D2 Shield	18	
D2-	D1+	17	
D1+	D1 Shield	16	
D1-	D0+	15	
D0+	D0 Shield	14	
D0-	CK+	13	
CK+	CK Shield	12	
CK-	CE Remote	11	
NC	DDC CLK	10	
DDC CLK	DDC DATA	9	
GND	GND	8	
+5V	HP DET	7	
SHELL2	SHELL2	6	
ABA-HDM-018-K01		5	



G-Note Montevina
Quanta Computer Inc.

Size	Document Number	Rev
Custom	HDMI PORT	2A
Date:	Thursday, April 09, 2009	Sheet 26 of 55

[illegible]

(39) BATLED_GREEN

(39) BATLED_AMBER

(39) SUSPEND_LED#

R555 221/F 4

R556 221/F 4

R557 221/F 4

LED1 GREEN

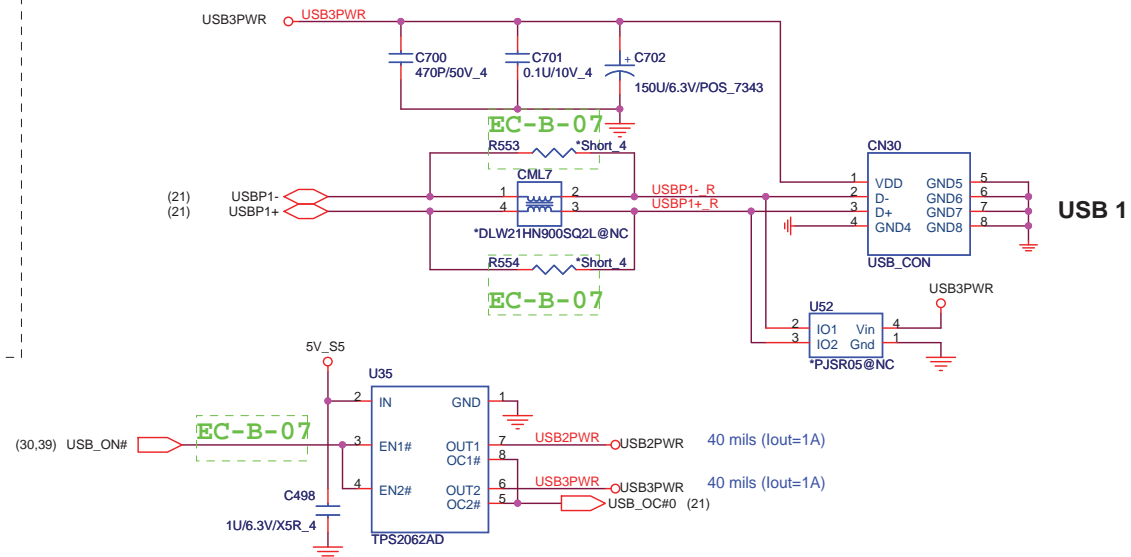
LED2 SUSPEND_LED_GREEN

RIGHT-ANGLE-LED

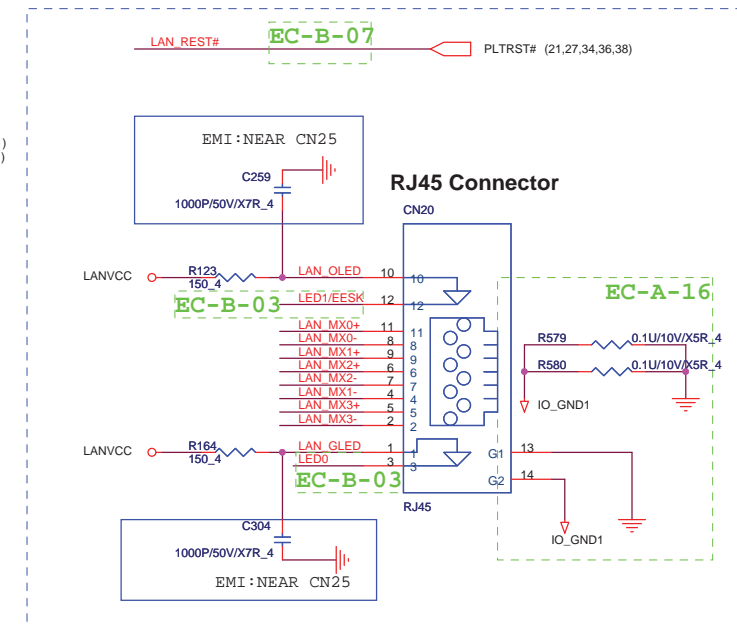
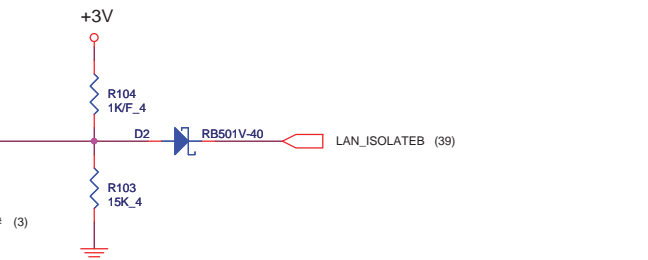
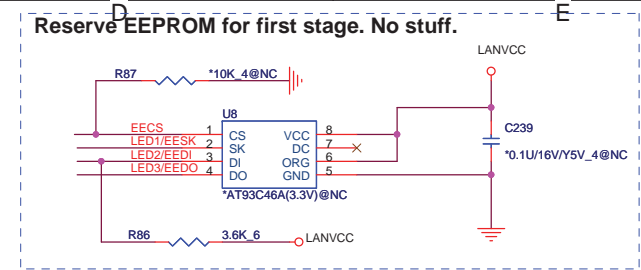
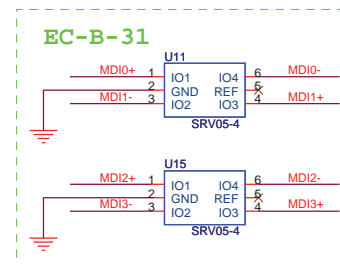
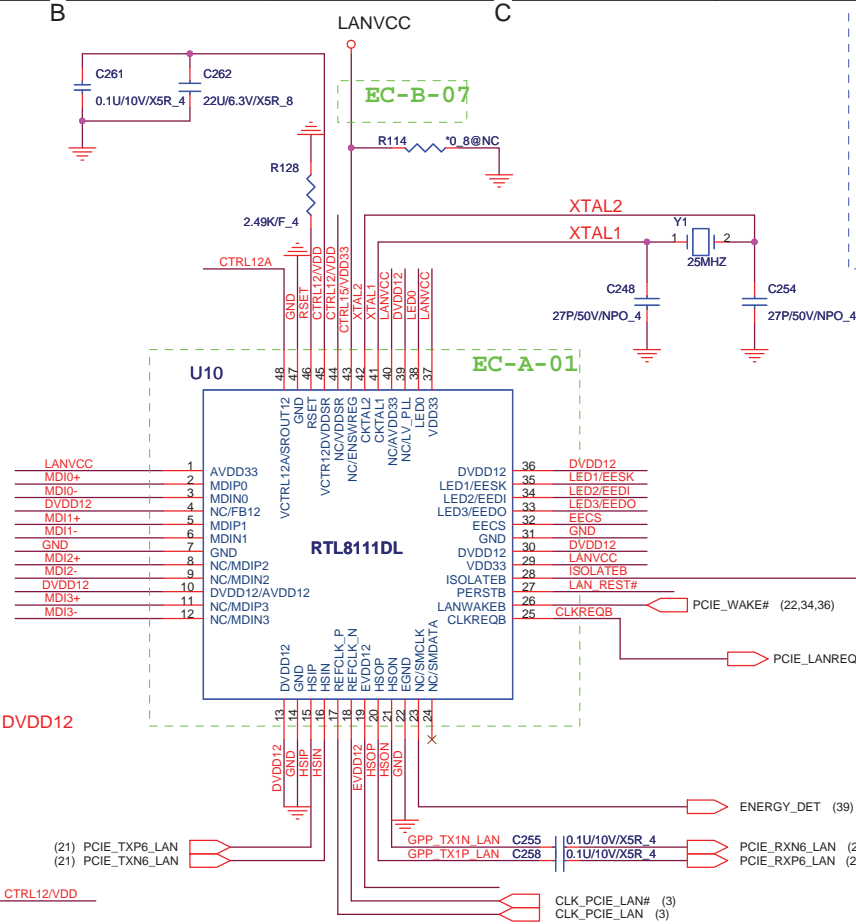
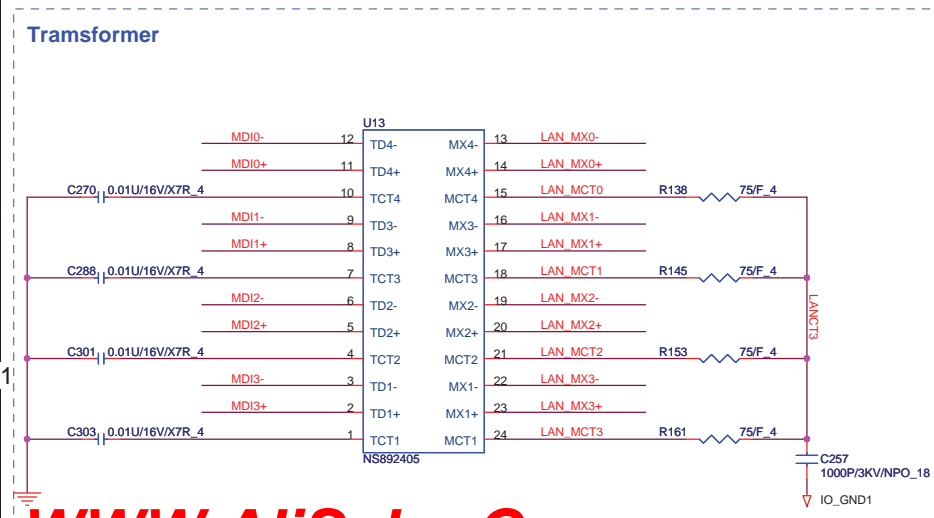
3VPCU

3VSUS

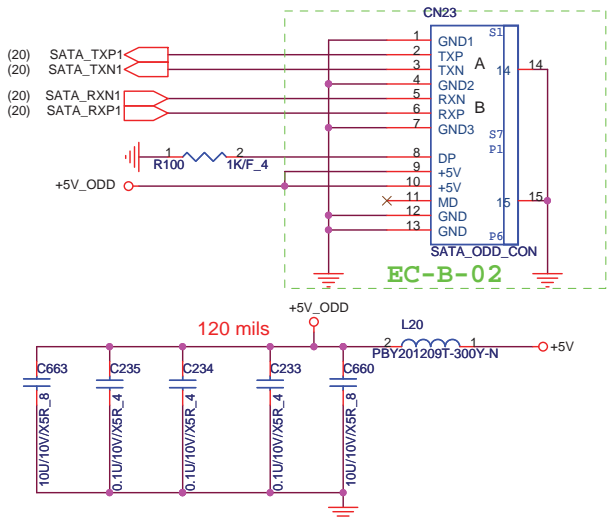
EC-B-30



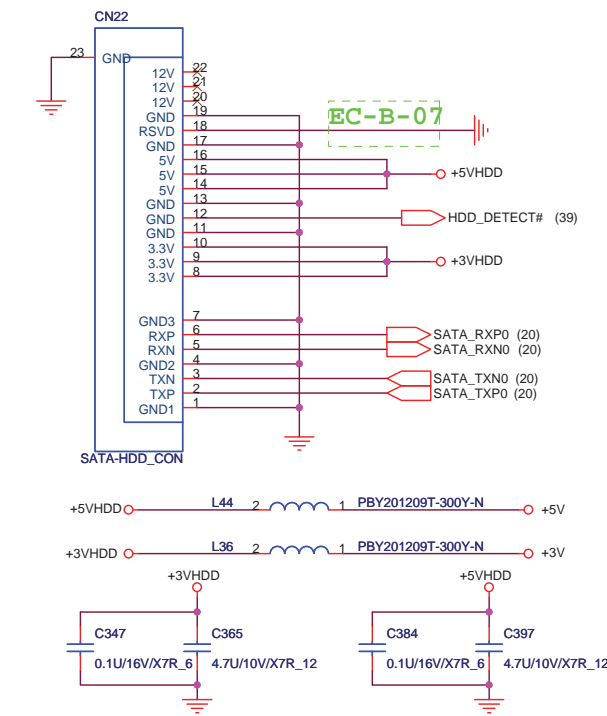
 <div style="display: inline-block; vertical-align: middle;"> <p>PROJECT :G NOTE</p> <p>Quanta Computer Inc.</p> </div>				
Size Custom	Document Number	<p>USB X2/SIM_CARD/LEDs/RF</p>		Rev 2A
Date:	Thursday, April 09, 2009	Sheet	27 of 55	



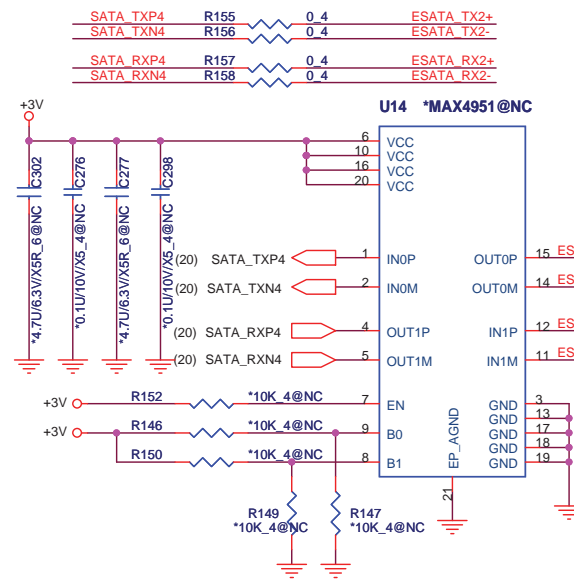
SATA CD-ROM



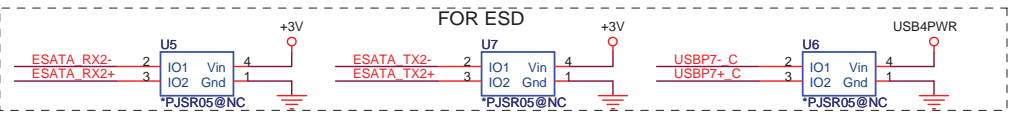
SATA-HDD



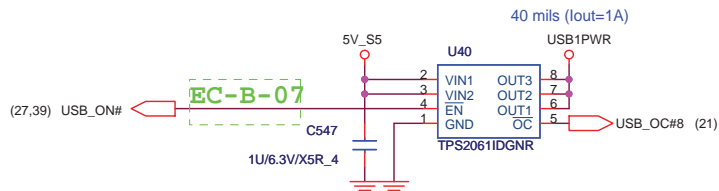
E-SATA RE-DRIVER



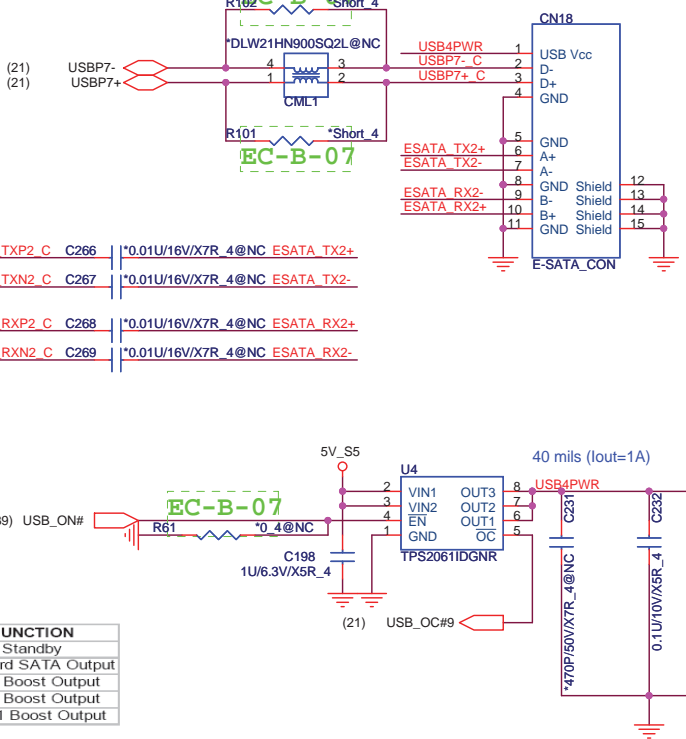
EN	B0	B1	FUNCTION
0	X	X	Standby
1	0	0	Standard SATA Output
1	1	0	Ch 0 Boost Output
1	0	1	Ch 1 Boost Output
1	1	1	Ch 0,1 Boost Output



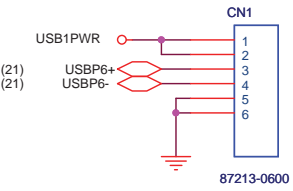
USB x1(on board)



eSATA PORT



REAR_USB/B connector

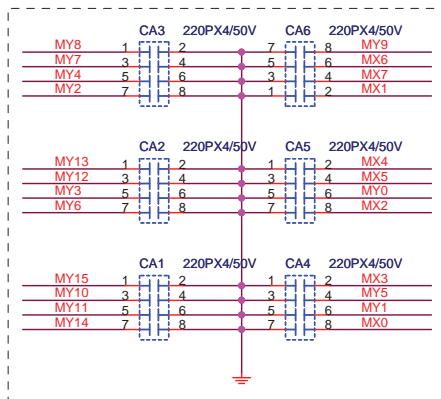
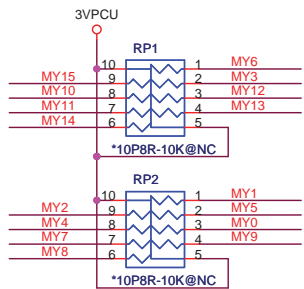
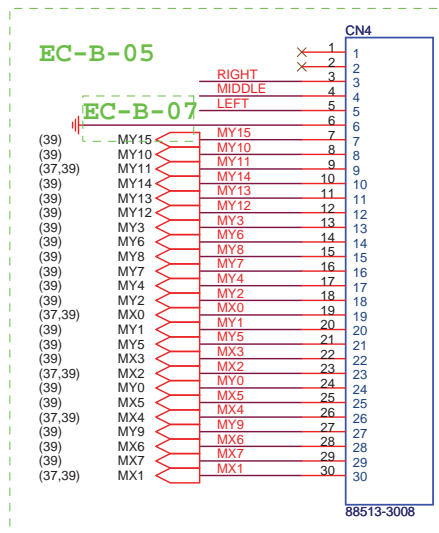


G-Note Montevina
Quanta Computer Inc.

Size B	Document Number	Rev 2A
SATA HDD/ ODD/ eSATA/USB		
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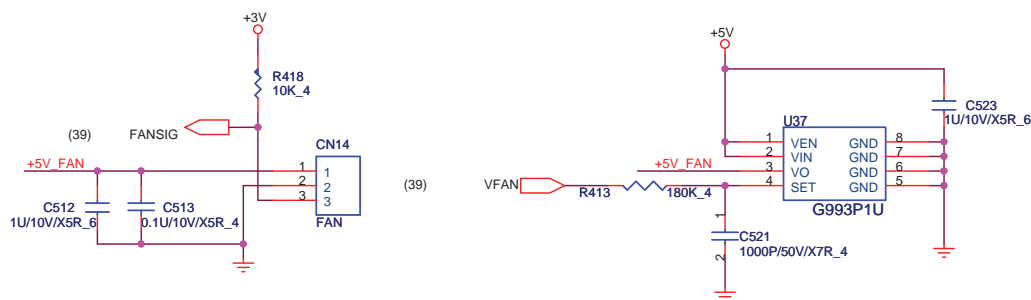
FAN, K/B, T/P & Track Point

KEYBOARD connector

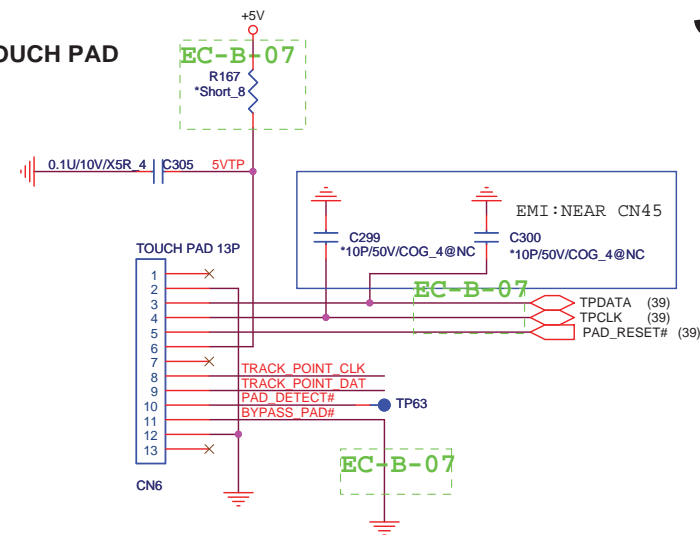


For EMI request

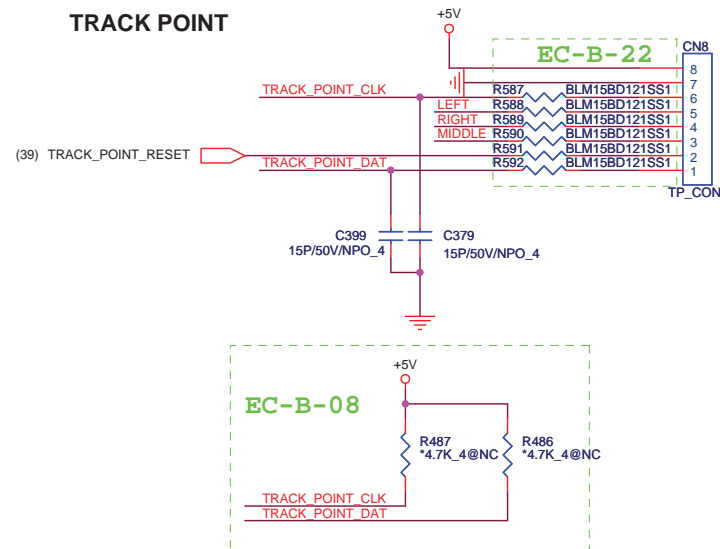
FAN Controller



TOUCH PAD



TRACK POINT

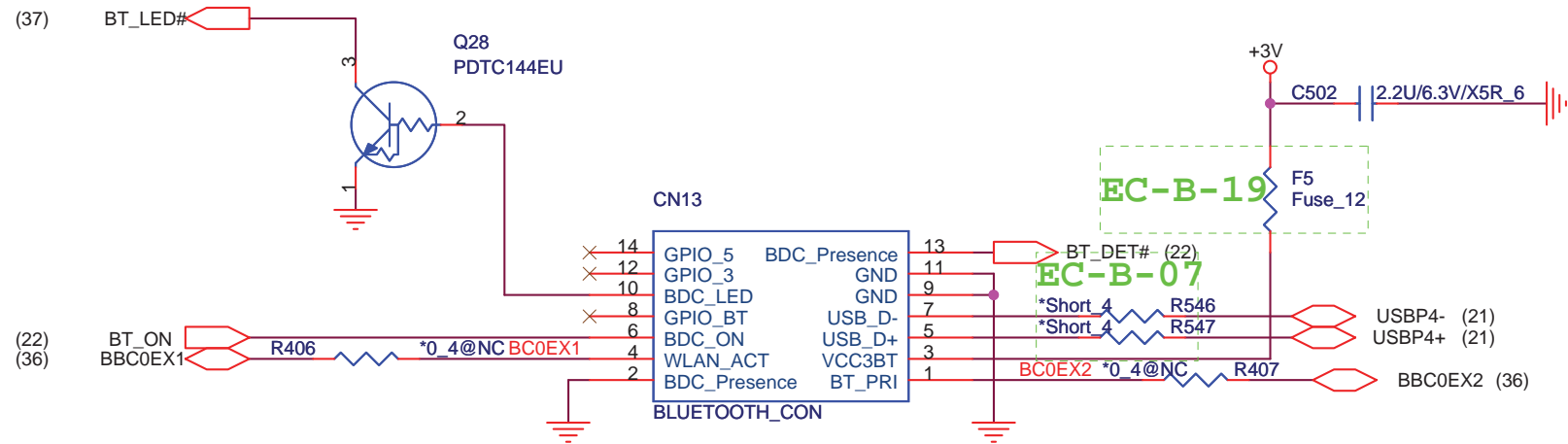


G-Note Montevina
Quanta Computer Inc.

Size B Document Number KB/ TP/ FAN Control Rev 2A

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BLUETOOTH

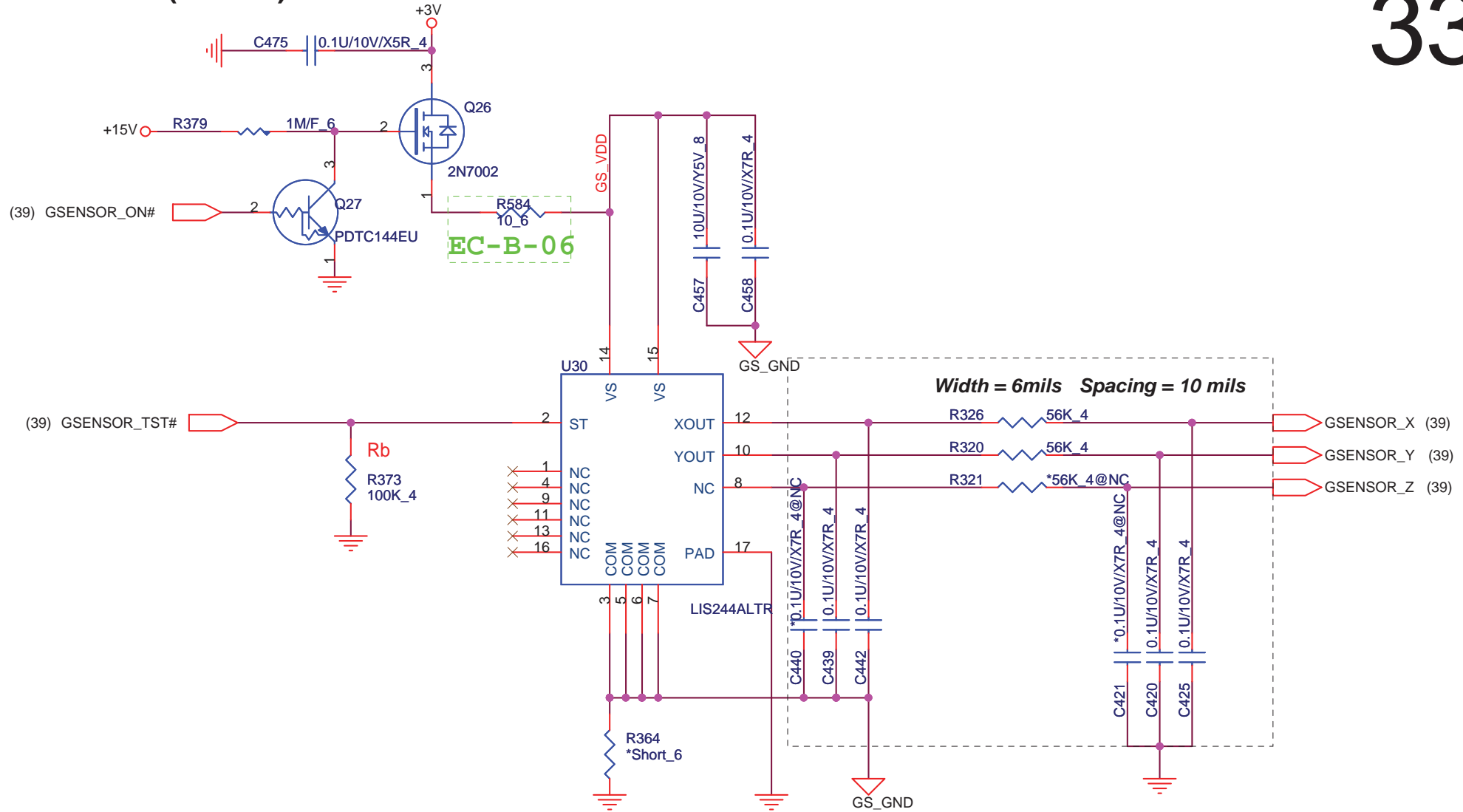


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Quanta Computer Inc.

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Date:	Thursday, April 09, 2009	Sheet 32 of 55

G-SENSOR (2-Axial)

33

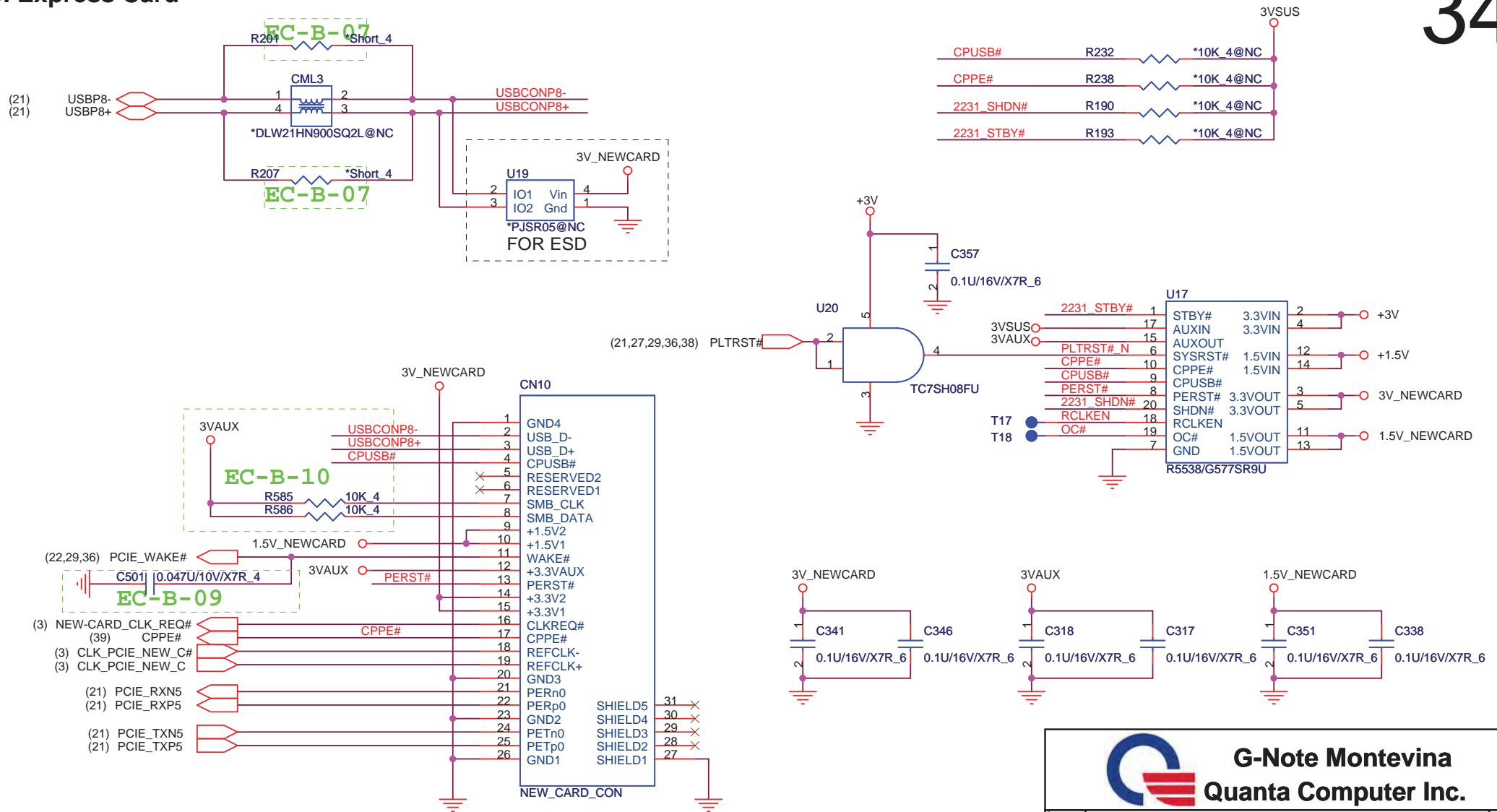



**G-Note Montevina
Quanta Computer Inc.**

Size A	Document Number	Rev 2A
	G-SENSOR	
Date:	Thursday, April 09, 2009	Sheet 33 of 55

PCI Express Card

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G-Note Montevina
Quanta Computer Inc.

Size	Document Number	Rev
Custom	PCI Express Card	2A
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Mini PCI-E Card

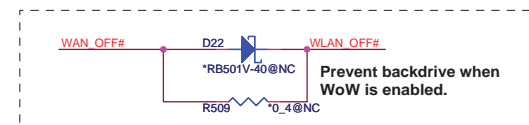
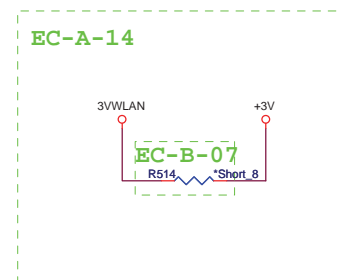
EC-B-04

35

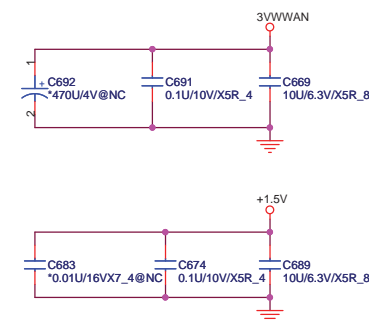
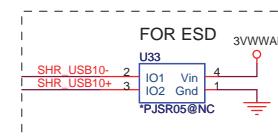


G-Note Montevina
Quanta Computer Inc.

Size A	Document Number Mini PCI-E Card	Rev 2A
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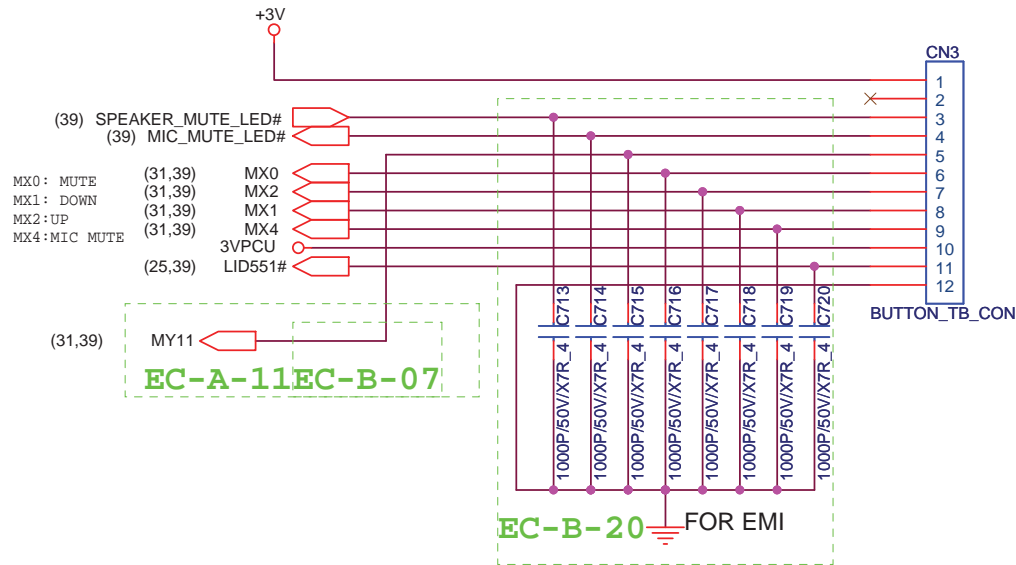
Robson



Daughter Boards for LEDs & Ports

37

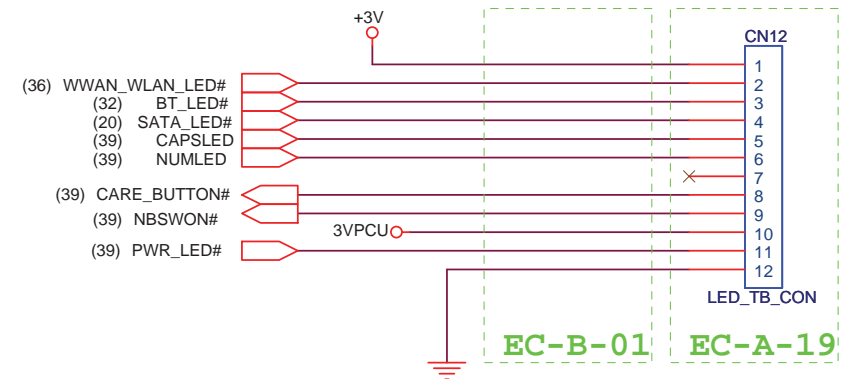
FFC TO KBD LEFT SIDE CONNECTOR




LOGO LED

EC-B-28

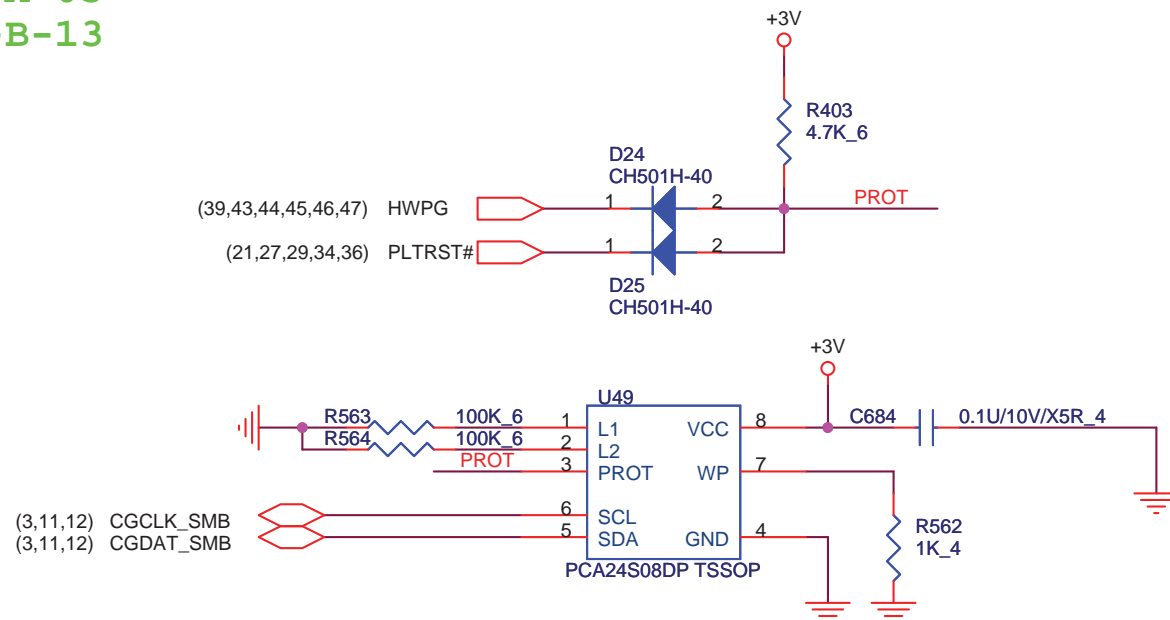
FFC TO LED RIGHT SIDE CONNECTOR



 G-Note Montevina Quanta Computer Inc.		Daughter Boards	Rev 2A

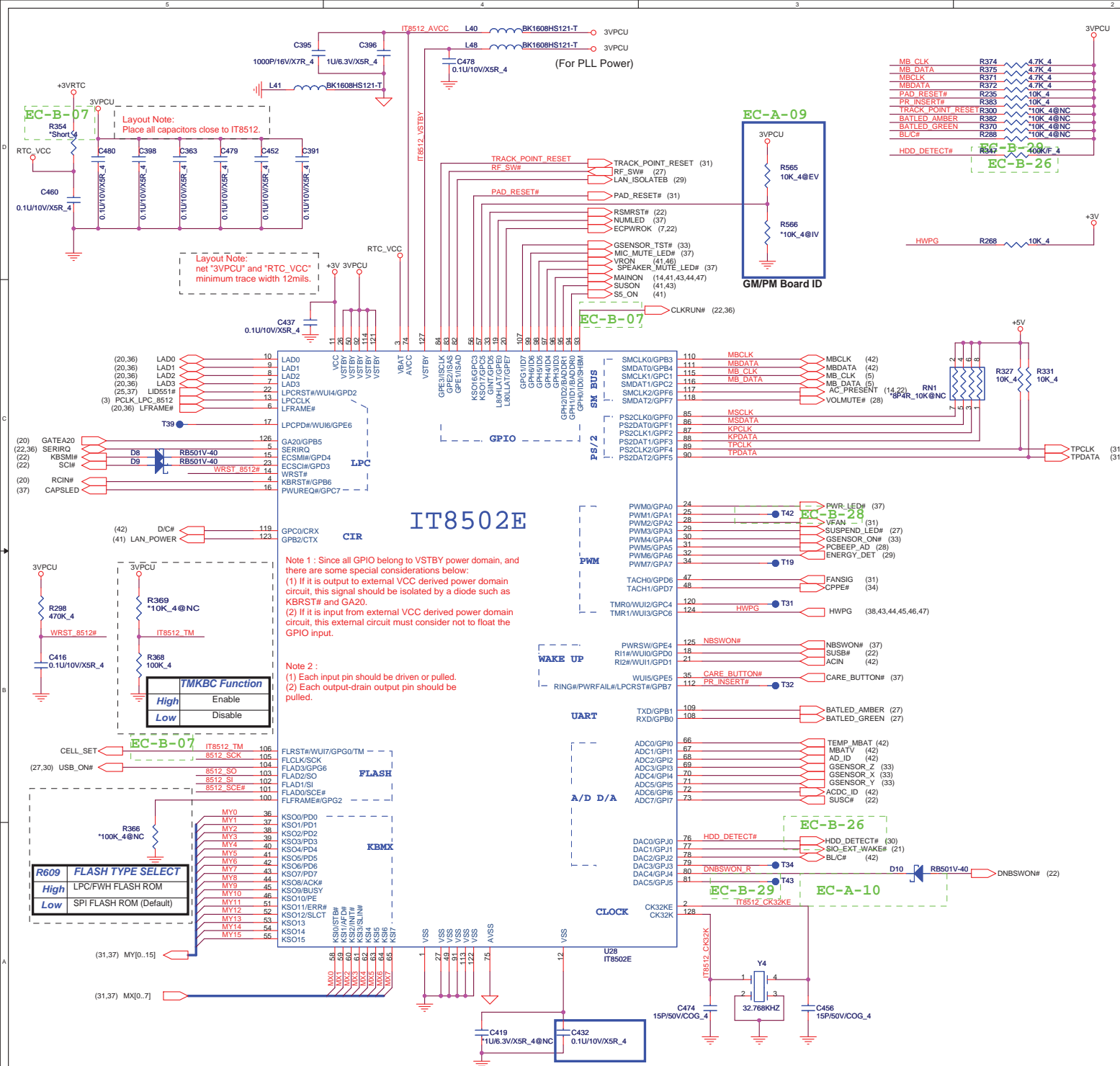
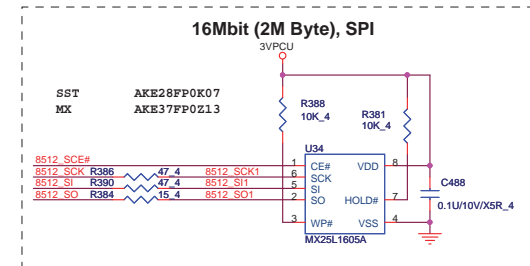
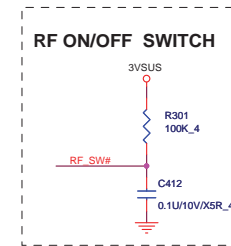
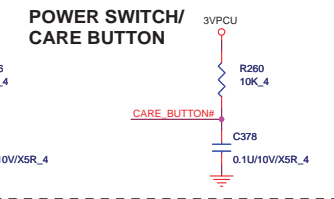
EC-A-03

EC-B-13



G-Note Montevina
Quanta Computer Inc.

Size A	Document Number RFID EEPROM	Rev 2A
Date: Thursday, April 09, 2009	Sheet 38 of 55	



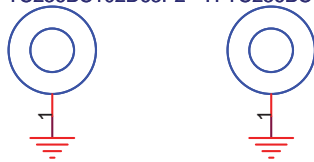
3mm minicard nut

HOLE10 H-Tc197BC142D102P2
HOLE12 H-Tc197BC142D102P2



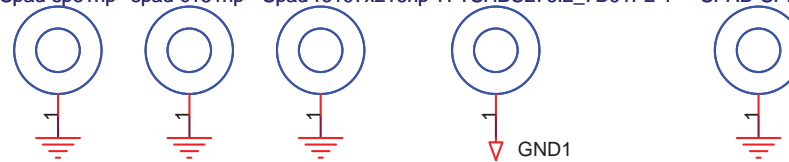
7mm minicard nut

HOLE26 H-Tc236BC102D63P2
HOLE27 H-Tc236BC102D63P2



PAD

GP1 *Spad-spe1np GP2 *spad-c181np GP3 *Spad-re197x216np
GP4 *H-TSHBC276I2_7D91P2-1 GP5 *SPAD-SPE3NP



40

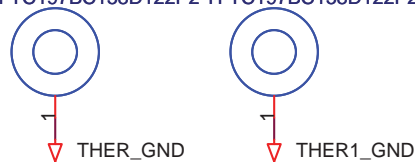
Hole for CPU support

HOLE20 *H-C142D142N HOLE21 *H-C142D142N
HOLE23 *H-C142D142N HOLE24 *H-C142D142N



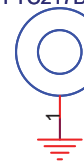
VGA nut

HOLE22 H-Tc197BC158D122P2 HOLE25 H-Tc197BC158D122P2



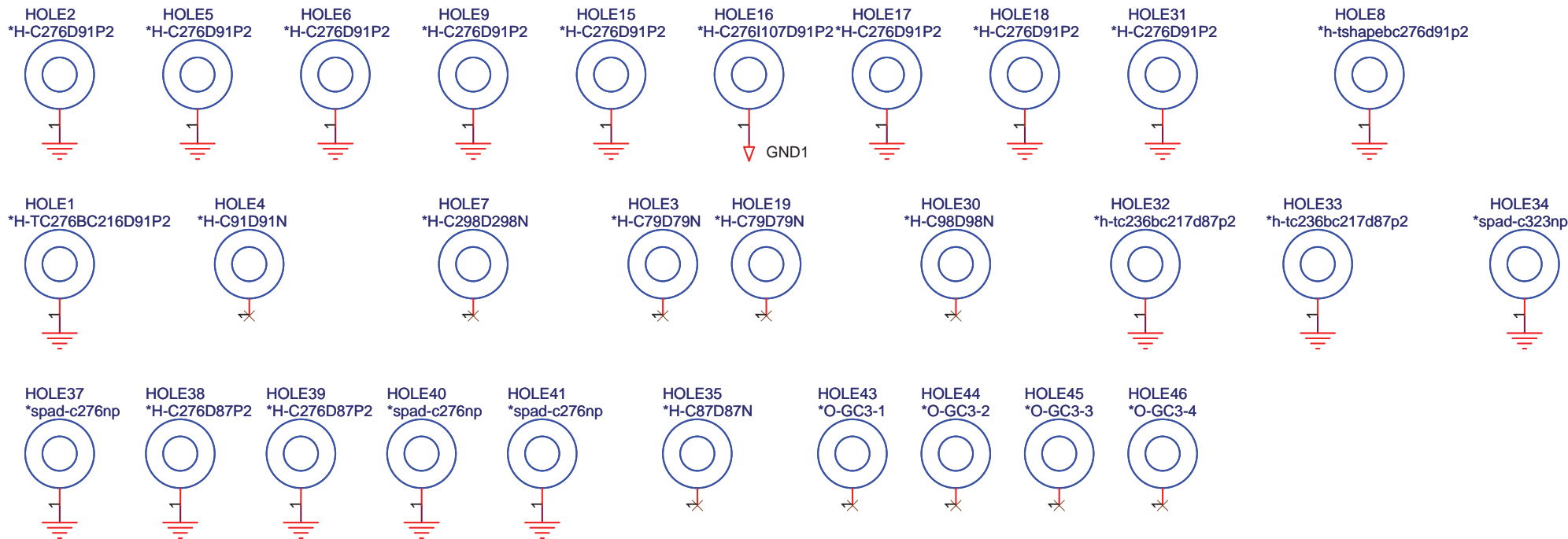
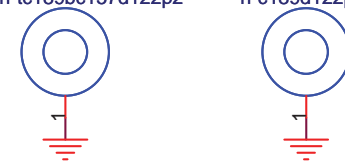
Bluetooth nut

HOLE14 H-Tc217BC154D118P2



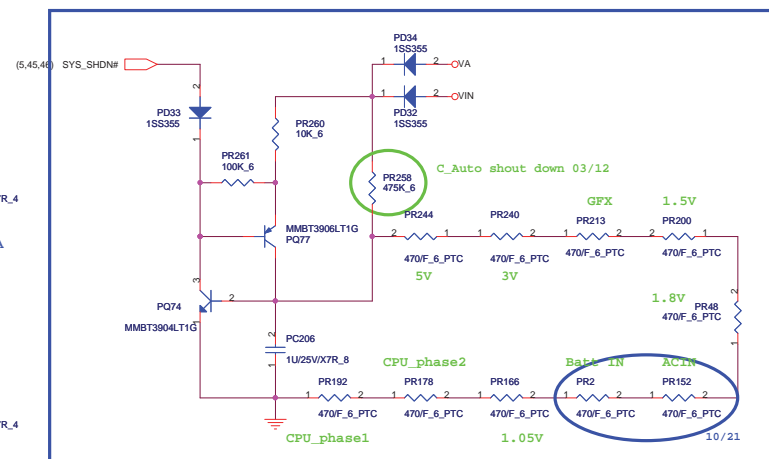
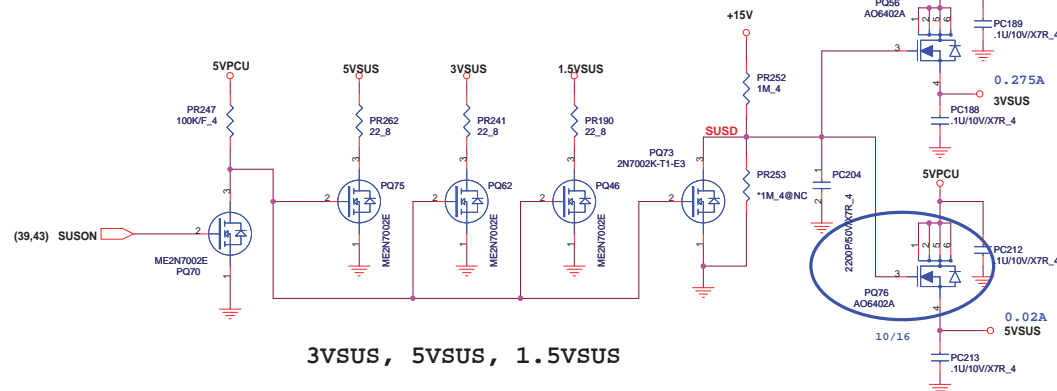
Card reader board nut

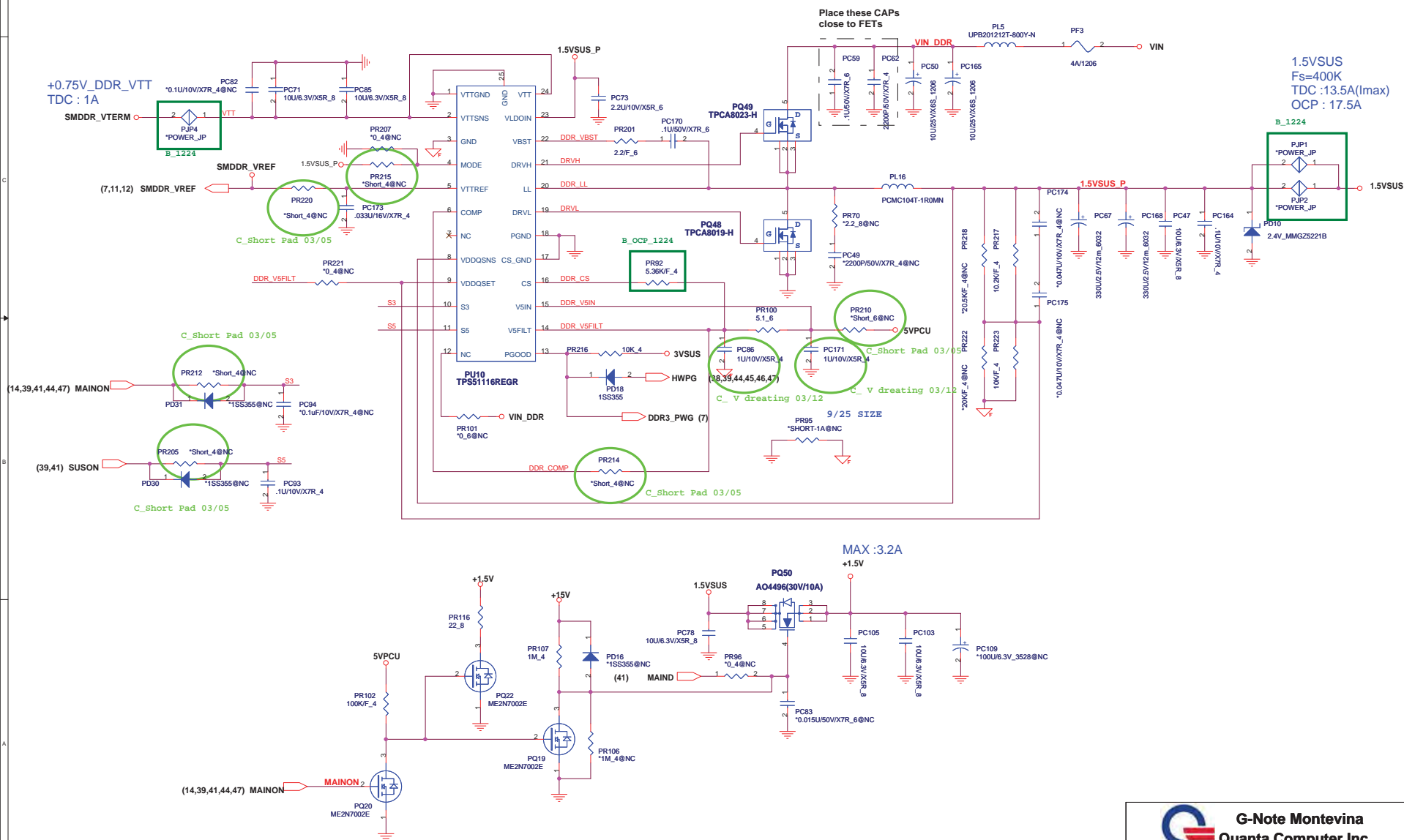
HOLE28 h-tc189bc157d122p2 HOLE29 h-c189d122pt

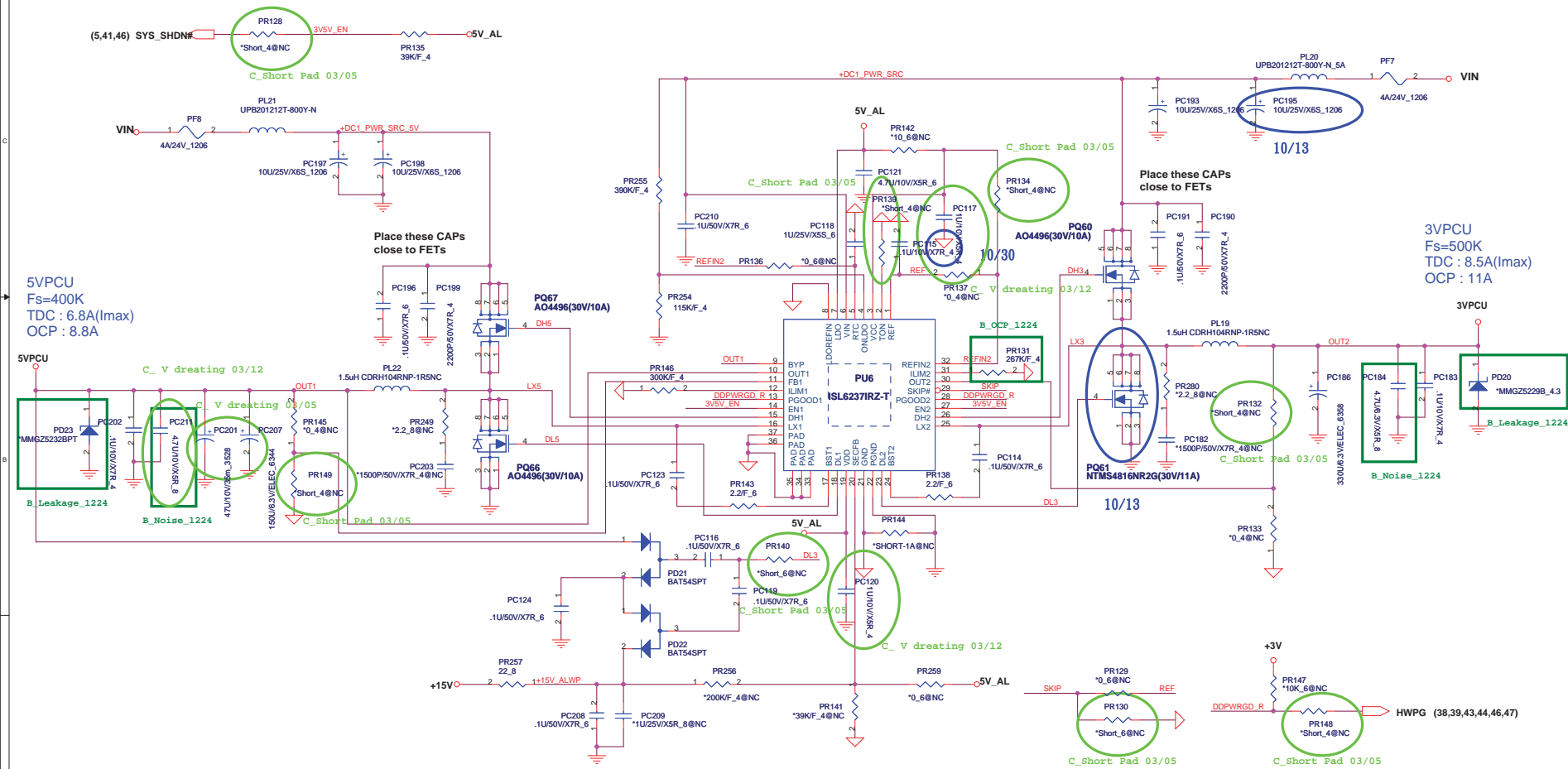


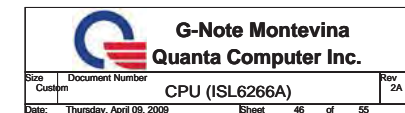
G-Note Montevina
Quanta Computer Inc.

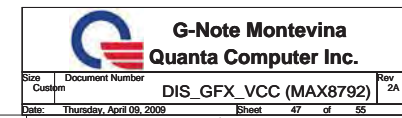
Size A	Document Number	Rev 2A
SCREW HOLE		
Date:	Friday, April 03, 2009	Sheet 40 of 55











Revision History

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Revision	Date	Phase	Change List	Release Schematic Date	Release Gerber File Date
1A		DV	Initial release		

Schematic Value Explanation Description :

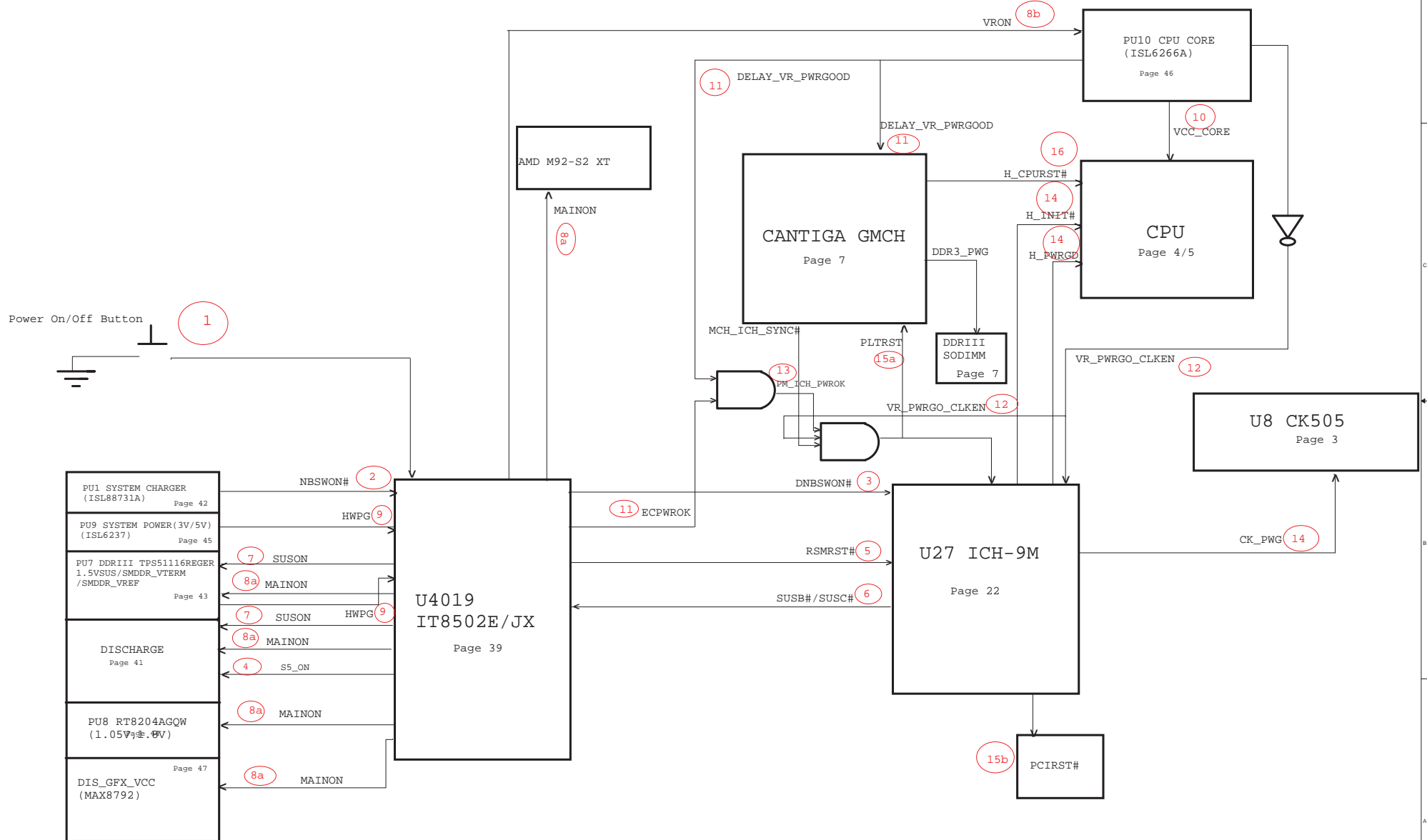
RESISTOR

Value	F	4	6	8	12	1210	*	Description
*1K/F_4	1%	0402 (1005)					DE POP	1K ohm 1% SMD 0402 package and DE POP
1K_6	5%		0603 (1608)				POP	1K ohm 5% SMD 0603 package and POP
1K_8	5%			0805 (2125)			POP	1K ohm 5% SMD 0805 package and POP
1K_12	5%				1206 (3216)		POP	1K ohm 5% SMD 1206 package and POP
1K_1210	5%					1210 (3225)	POP	1K ohm 5% SMD 1210 package and POP

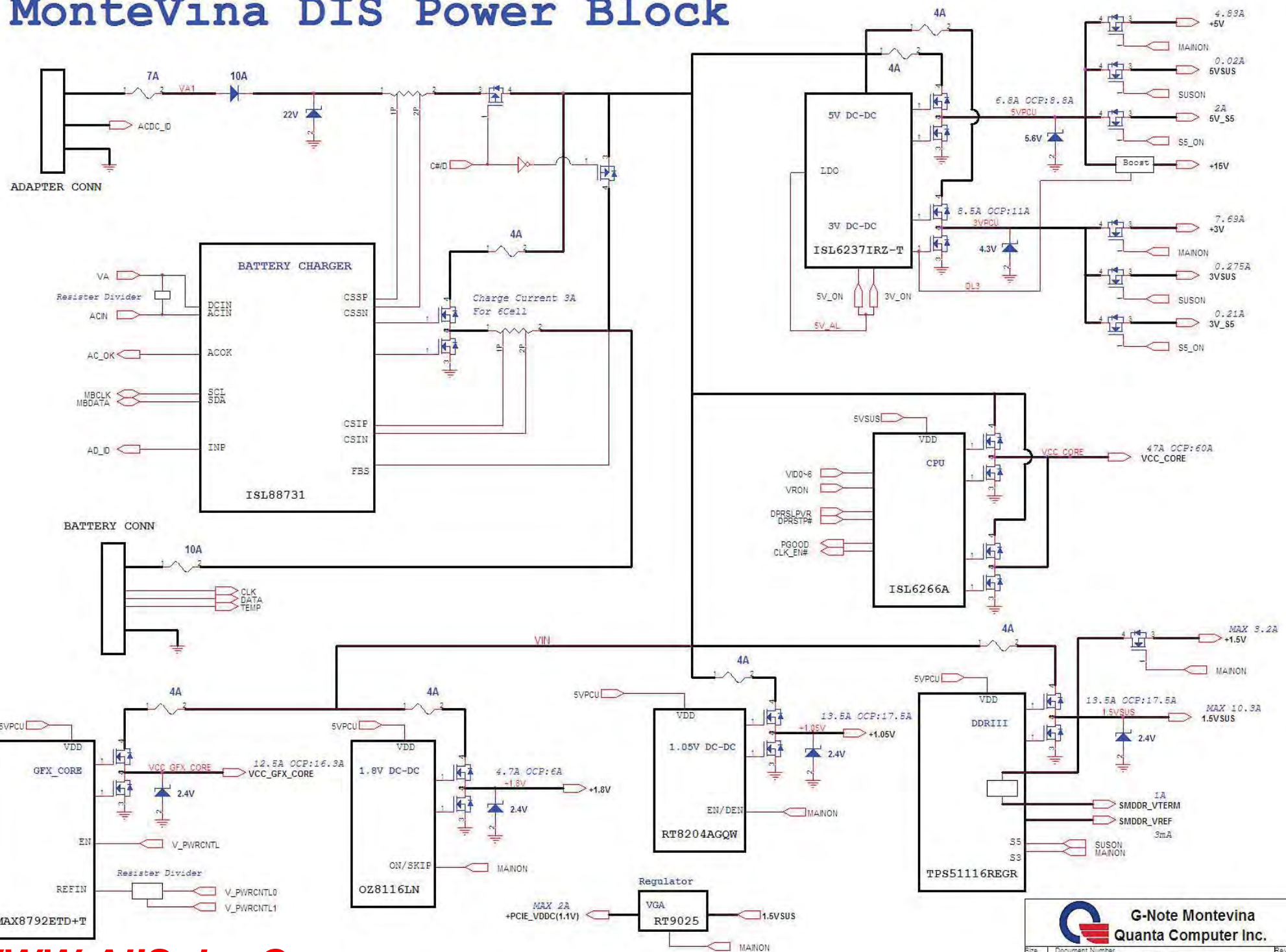
CAPACITOR

Value	Voltage	Material	6				*	Description
*0.1U/10V/X5R_4	10V	X5R	0402 (1005)				DE POP	0.1UF 10V X5R SMD 0402 package DE POP
1U/25V/X7R_6	25V	X7R	0603 (1608)				POP	0.1UF 25V X7R SMD 0603 package POP

[illegible]



MonteVina DIS Power Block



EC NO.	PG.	DATE	PART REFERENCE	DESCRIPTION
EC-A-01	42	12/24	PR10	Change Footprint
EC-A-02	42	12/24	PR12	Change to 2.2 ohm reduce phase ring
EC-A-03	42	12/24	PD6	Delete Footprint
EC-A-04	43	12/24	PJP1,PJP2,PJP4	Change Footprint
EC-A-05	43	12/24	PR92	Change to 5.36K for OCP
EC-A-06	44	12/24	PR151	Change to 2.8K for OCP
EC-A-07	44	12/24	PJP3,PJP9,PJP10	Change Footprint
EC-A-08	45	12/24	PR131	Change to 267K for OCP
EC-A-09	45	12/24	PC134,PC211	Change to 4.7u reduce H.F. noise reduce
EC-A-10	45	12/24	PD20,PD23	NA to reduce leakage current
EC-A-11	46	12/24	PR65	Change to 12.1K for OCP
EC-A-12	46	12/24	PR85	Change to 11.3K for frequency 300KHz
EC-A-13	47	12/24	PJP5,PJP6,PJP7	Change Footprint
EC-A-14	47	12/24	PR225	Change to 80.6K for OCP
EC-A-15	47	12/24	PR228,PR229,PR232	Change for VID point setting



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Size A	Document Number EC list	Rev 2A
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		EC NO.	PG.	DATE	PART REFERENCE	DESCRIPTION
A stage	2008	EC-A-01	29	12/08	U10	Change LAN IC footprint from (LQFP48-9X9-4)0.4 to (LQFP48-9X9-5)0.5 pitch
		EC-A-02	28	12/08	U32	Change Codec IC footprint to qfn48-7x7-5-58p-0_9h.(Add 9 via at the center PAD of original IC footprint)
		EC-A-03	38	12/08	U49	Change U49 schematic by adding R562,R563,R564 and D24 to solve F4 error code issue.
		EC-A-04	25	12/08	R196	Delete CCD_ON which was use to control CAM_VCC by change R196 from depop to pop and delete U16,C353,R204,R211,R219,R221.
		EC-A-05	14	12/08	R399	Add R399 to pull low CPIO_19_CTF according to AMD FAE suggestion.
		EC-A-06	22	12/08	R347	Replace CCD_ON with HDD_DETECT#, original HDD_DETECT# will cause FF error code while attached HDD.
		EC-A-07	03	12/08	R313	Change to 4.7Kohm To solve N.B. cannot get correct FSB frequency selection (error coed 02)
		EC-A-08	36	12/08	CN11	Add CLKRUN#,SERIRQ,LPC_LDRQ0#,LPC_PD#
		EC-A-09	39	12/08	R565,R566	Add adapter selection board ID by adding R565 an R566.
		EC-A-10	25	12/08	Q13	Change Q13 connection.
		EC-A-11	37	12/08	R567,R568	Add optional resistor between MY11 and MY13
		EC-A-12	28	12/11	C465,C426,C427, C506,C507,C499, C495,C497,C455	Change footprint from 0805 to 0603 per mechanical request.
		EC-A-13	25	12/19	CN5	Connect LCD connector shielding to GND for better EMI performance.
		EC-A-14	36	12/23	C679,Q35,Q36A, Q36B,R506,R510, R511,R519,R521	Delete redundunt schematic to save space for layout.
		EC-A-15	05	12/23	C708,C709,Q38, Q39,Q40,R569, R570,R571,R572, R573,R574,U53	Change thermal sensor.
		EC-A-16	29	12/23	R579,R580	Add IO_GND1 for LAN connector per EMI request.
		EC-A-17	26	12/24	R581,R582,R583, Q41,Q42	Change HP_DET schematic for better ESD protection and prevent floating
		EC-A-18	28	12/30	U54,D26,D27, C710	Add new schematic to prevent "POP" sound.
			EC-A-19	37	12/30	CN12
2009	EC-A-20	27	01/07	CN27	Change pin define of CN27 by intercept GND pin between differential pair per EMI request.	



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Quanta Computer Inc.

Size B	Document Number EC list	Rev 2A
Date: Tuesday, March 03, 2009	Sheet 53 of 55	

2009	B stage	EC NO.	PG.	DATE	PART REFERENCE	DESCRIPTION
		EC-B-01	37	02/24	CN2	Delete CN2(extra right button/b connector)
		EC-B-02	30	02/24	CN19	Delete CN23 (extra ODD connector)
		EC-B-03	29	02/24	CN20	Swap PIN3 and PIN14 to correct the LED behavior.
		EC-B-04	3 21 35	02/26	C664,C665,C667, C672,C685,CN9, R488,R489,R491, R492,R494,R496, R515,U46,C324, C325,RP4,R293	Delete one minicard slot per customer request.
		EC-B-05	30	02/26	CN4	Reverse the pin define of KB connector for the conveninece of assembly.
		EC-B-06	33	02/26	R584	Insert 10 ohm resistor(10_6) between FET and VDD(G-sensor) for Analog noise reduction.
		EC-B-07	03 10 13 14 17 22 23 24 25 27 28 29 30 31 32 36 39	02/27	C403,L2,L56,R322, R105,R113,R127,R131, R133,R142,R143,R151, R159,R16,R160,R167, R180,R186,R579,R261, R269,R329,R339,R355, R356,R214,R22,R252, R255,R267,R550,R278, R282,R284,R286,R294, R312,R314,R33,R332, R338,R340,R547,R351, R353,R354,R367,R377, R401,R402,R405,R415, R429,R435,R436,R454, R493,R502,R505,R514, R518,R522,R567,R568 R525,R529,R531,R534, R537,R580,R543,R545, R546,R9,R38,R65,RP3, RP4,RP5,RP6,RP7, R569,R48,R362,R92, R119,R32,R36,R444, R447,R485R162,R163, R189,R192,R200,R206, R216,R227,R237,R244, R548,R549,R551,R552, R553,R554,R60,R101, R102,R364,R201,R207	Delete redundant 0 ohm or change it to short pad in the circuit.



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2009	EC NO.	PG.	DATE	PART REFERENCE	DESCRIPTION
B stage	EC-B-08	31	03/03	R486,R487	Add but no assembly
	EC-B-09	34	03/03	C501	Add
	EC-B-10	34	03/03	R585,R586	Disconnect SMB and change it to pull high 10K to 3VAUX
	EC-B-11	36	03/03	R523,R524	Change to 4.7K and pull high to 3VWLAN
	EC-B-12	36	03/03	R526,R528	Change to 4.7K and pull high to 3VWWAN
	EC-B-13	38	03/03	C684,D24,D25, R403,R562,R563, R564,U49	Change RFID IC package to TSSOP.
	EC-B-14	28	03/04	C711,C712	Reserve 1u capacitor between L+,L- and R+,R- for EMI
	EC-B-15	22	03/04	R223,R224, R239,R250	Change board ID for SIT stage.
	EC-B-17	28	03/06	R272,R273,R274, R275	Add R272,R273,R274,R275 for EMI
	EC-B-18	26	03/06	D21	
	EC-B-19	25 32	03/12	F3,F4,F5	
	EC-B-20	37	03/09	C713,C714,C715, C716,C717,C718, C719,C720	Add capacitor for EMI
	EC-B-21	03	03/09	R357,R323,R279	Due cpu clock already fix,so delete redundant parts.
	EC-B-22	31	03/10	R587,R588,R589, R590,R591,R592	Add EMI filter for RF
	EC-B-23	44	03/16	PD17	Assembly PD17 to correct VGA graphic power off sequence.
	EC-B-24	25	03/12	R593,C311,C312	Add bead for EMI
	EC-B-26	24	03/17	D14,D15,D16, D17,D18,D19, D20	Change CRT ESD protection from Switching Diode to Transient Voltage Suppressors.
	EC-B-27	22,39	03/30	R347	Change HDD_DETECT# connection from ICH9 to KBC
	EC-B-28	25,37 39	04/07	Q17,Q19,R169, R174,R175	Delete LOGO Led for cost down
	EC-B-29	25 39	04/07	R541,C309,Q13, Q14,R165,R166	Delete THINK light for cost down
	EC-B-30	27	04/08	R555,R556,R557	Fine tune battery and suspend LED brightness
	EC-B-31	29	04/10	U11,U15	Replace TVS between transformer and LAN IC for Hi-pot test



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